Photo DMOS-FET Relay

Description

The **LT736** is a miniature 2-Form A solid state relay in a 8 pin SMD package that employs optically coupled MOSFET technology to provide 3750V of input to output isolation. The optically coupled input is controlled by a highly efficient GaAlAs infrared LED and MOS FETs on the output side.

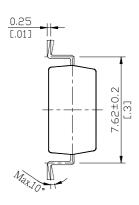
Features

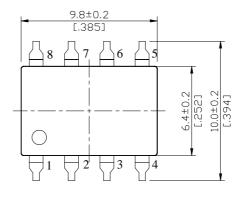
- Low driver power requirements (TTL/CMOS Compatible)
- High reliability
- Arc-Free with no snubbing circuits
- 3750Vrms Input/Output isolation
- Tape & Reel version available

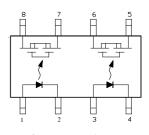
Applications

- Telecommunications (PC, Electronic notepad)
- Measuring and Testing equipment
- Industrial control
- Security equipments
- High speed inspection machine

Outline Dimensions







1,3. LED Anode

2,4. LED Cathode

5,6. Drain (MOS FET)

7,8. Drain (MOS FET)

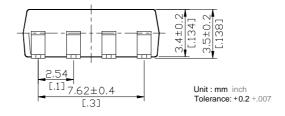








Photo DMOS-FET Relay Specifications <u>Part Name: LT736</u>

(Load voltage: 100V / Load current: 400mA)

Absolute Maximum Ratings (Ambient Temperature: 25°C)

Item		Symbol	Value	Units	Note
	Continuous LED Current	IF	50	mA	
Input	Peak LED Current	IFP	1000	mA	f=100Hz, duty=1%
	LED Reverse Voltage	V_R	5	V	
	Input Power Dissipation	PIn	75	mW	
	Load Voltage	$V_{\rm L}$	100	V(AC peak or DC)	
Output	Load Current	IL	400	mA	
Output	Peak Load Current	IPeak	1.0	A	100ms(1 pulse)
	Output Power Dissipation	Pout	450	mW	
Total Power Dissipation		PT	500	mW	
I/O Breakdown Voltage		V _{I/O}	3750	Vrms	RH=60%, 1min
Operating Temperature		Topr	-40 to +85	$^{\circ}\mathbb{C}$	
Storage Temperature		Tstg	-40 to +100	$^{\circ}\mathbb{C}$	
Pin Soldering Temperature		Tsol	260	$^{\circ}\!\mathbb{C}$	10 sec max.

Electrical Specifications (Ambient Temperature: 25°C)

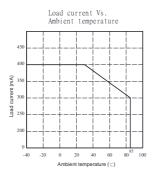
Item		Symbol	MIN.	TYP.	MAX.	Units	Conditions
Input	LED Forward Voltage	V_F		1.2	1.5	V	I _F =10mA
	Operation LED Current	IF on		0.5	3.0	mA	
	Recovery LED Current	${ m IF}$ off		0.35	3.0	mA	
	Recovery LED Voltage	$V_{F \rm off}$	0.7			V	
Output							IF=5mA,IL=100mA,
	On-Resistance	Ron		2.0	2.5	Ω	Time to flow is within
							1 sec.
	Off-State Leakage	ILeak			1.0	uA	V _L =Rating
	Current						
	Output Capacitance	Cout		22		pF	VL=0, f=1MHz
Transmis	Turn-On Time	Ton		0.3	0.6	ms	IF=5mA, IL=100mA,
sion	Turn-Off Time	Toff		0.05	0.1	ms	
Coupled	I/O Isolation Resistance	Ri/o	10 ¹⁰			Ω	DC500V
	I/O Capacitance	Ci/o		0.8	1.5	pF	f=1MHz

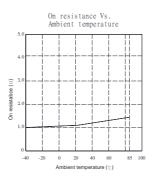


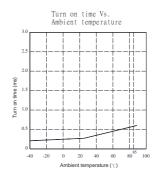


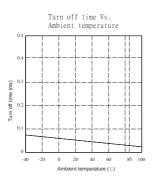


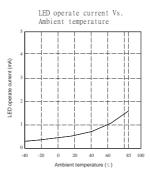
Reference Data

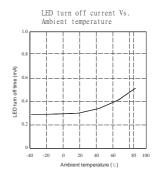


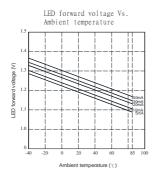


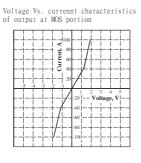


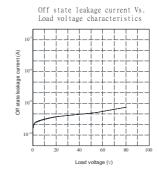


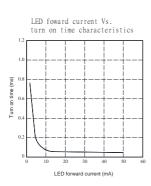


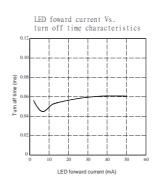


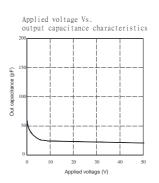










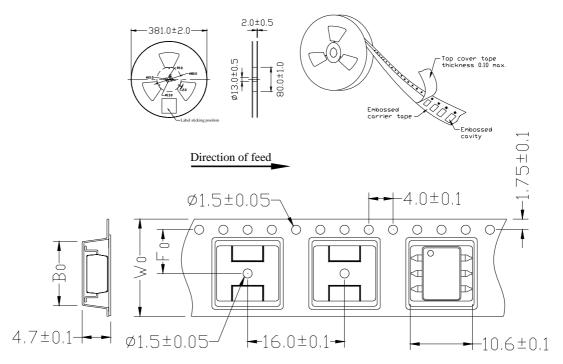






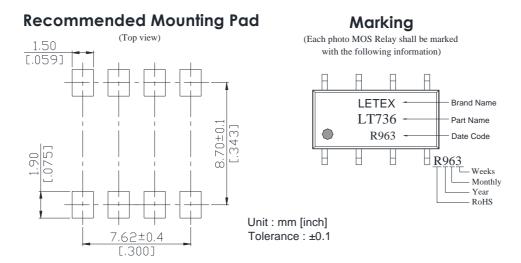


Taping Specifications for Surface Mount Devices



Unit: mm

TYPE	Bo±0.1	Fo±0.1	Wo±0.1	13"REEL/PCS
4P	5.3	7.5	16	1000
6P	9.4	7.5	16	1000
8P	10.3	11.5	24	1000



- Note: 1. There shall be leader of 230 mm minimum which may consist of carrier and or cover tape follower by a minimum of 160 mm of carrier tape sealed with cover tape.
 - 2. There shall be a minimum of 160 mm of empty component pockets sealed with cover tape.
 - 3. Devices are pockets in accordance with EIA standard EIA-481-A and specifications given above.





