



**BRIGHTTEK**  
**BRIGHTTEK (EUROPE) LIMITED**

*Brighten Up The World With LED!*



ISO/TS 16949:2009



BS EN ISO 14001:2004



QC 080000 IECQ HSPM

## PRODUCT DATASHEET



- ▶ CSP CHIP LED
- ▶ 1818 0.92t Series
- ▶ PC Selective Yellow (580nm)

# NOY53S42SY (Selective Yellow)



Release Date: 22 March 2021 Version: A1.0



### 1818 0.92t Series



#### FEATURES:

- **Package:** Ceramic High Power CSP Package
- **Forward Current:** 700mA
- **Forward Voltage (typ.):** 3.0V
- **Luminous Flux (typ.):** 270lm@700mA
- **Colour:** PC Selective Yellow
- **Wavelength (typ.):** 580nm
- **Viewing angle:** 115°
- **Materials:**
  - Die: Flip-Chip InGaN
  - Resin: Silicon (Yellow Diffused)
  - L/T Finish: Ag plated
- **Operating Temperature:** -40~+125°C
- **Storage Temperature:** -40~+125°C
- **Grouping parameters:**
  - Forward Voltage
  - Luminous Flux
  - CIE Chromaticity
- **Soldering methods:** IR Reflow
- **Preconditioning:** MSL2 according to J-STD020
- **Packing:** 8mm tape with Max.2000pcs /reel, ø180mm (7")

#### APPLICATIONS:

- Decorative Lighting
- Portable Lighting
- Outdoor Lighting
- Commercial Lighting
- Indoor Lighting
- Industrial Lighting

## CHARACTERISTICS:

### Absolute Maximum Characteristics (Ta=25°C)

Parameter	Symbol	Ratings	Unit
DC Forward Current	I <sub>F</sub>	1000	mA
Pulse Forward Current 1/10 Duty @ 1KHz	I <sub>PF</sub>	1500	mA
Power Dissipation	P <sub>D</sub>	3.4	W
Reverse Voltage	V <sub>R</sub>	5	V
Junction Temperature	T <sub>j</sub>	150	°C
Thermal Resistance Junction to Solder Point	R <sub>th(j-s)</sub>	10	°C/W
Temperature Coefficient of Voltage	---	-2.5	mV/°C
Operating Temperature	T <sub>OPR</sub>	-40~+125	°C
Storage Temperature	T <sub>STG</sub>	-40~+125	°C

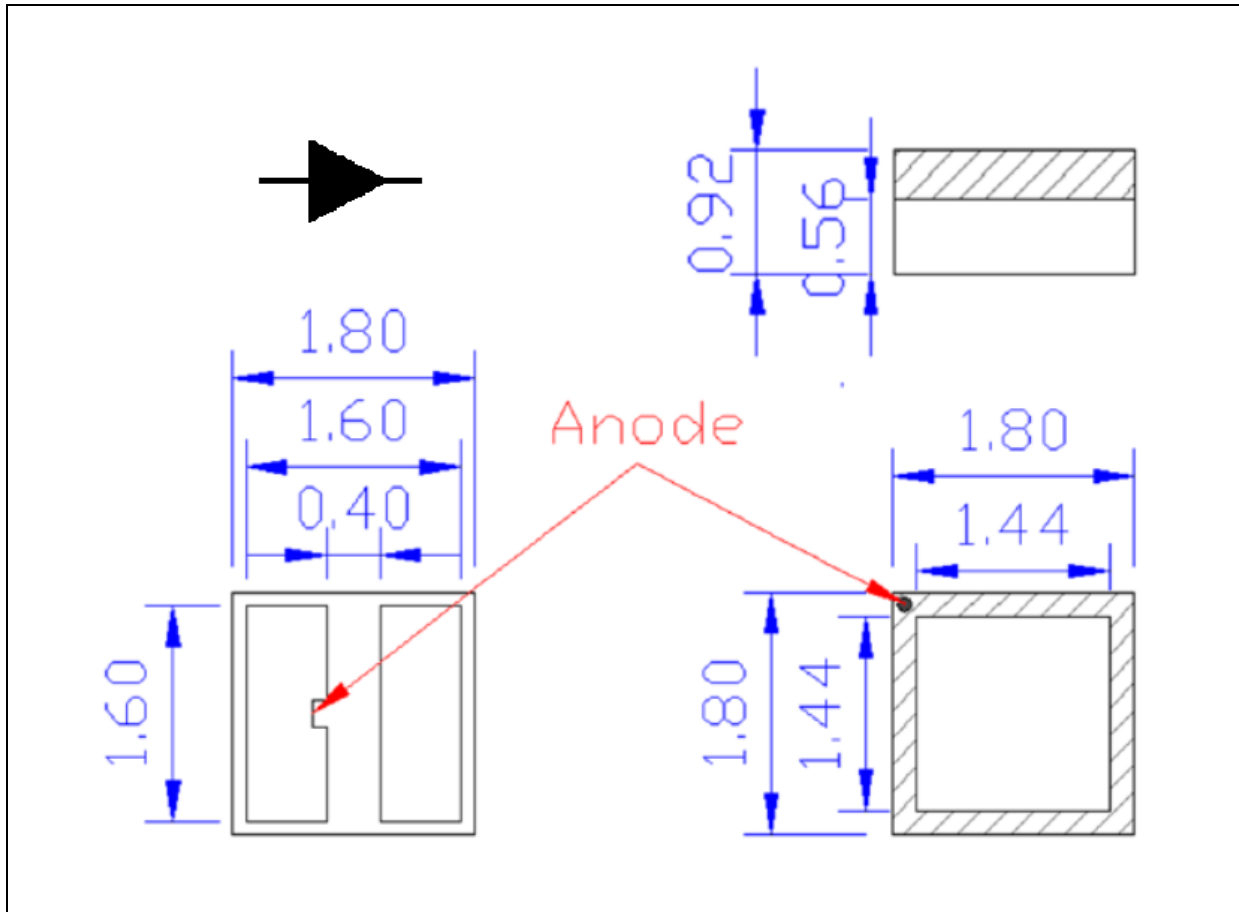
### Electrical & Optical Characteristics (Ta=25°C)

Parameter	Symbol	Values			Unit	Test Condition
		Min.	Typ.	Max.		
Forward Voltage	V <sub>F</sub>	2.8	3.0	3.4	V	I <sub>F</sub> =700mA
Luminous Flux	Φ <sub>v</sub>	250	270	290	lm	I <sub>F</sub> =700mA
Chromaticity Coordinates	X	0.4673	---	0.5438	---	I <sub>F</sub> =700mA
	Y	0.4447	---	0.5190		
Dominant Wavelength	λ <sub>D</sub>	---	580	---	nm	I <sub>F</sub> =700mA
Viewing Angle	2θ <sub>1/2</sub>	---	115	---	deg	I <sub>F</sub> =700mA

1. Luminous flux (Φ<sub>v</sub>) ±7%, Forward Voltage (V<sub>F</sub>) ±0.05V, Viewing angle(2θ<sub>1/2</sub>) ±10°

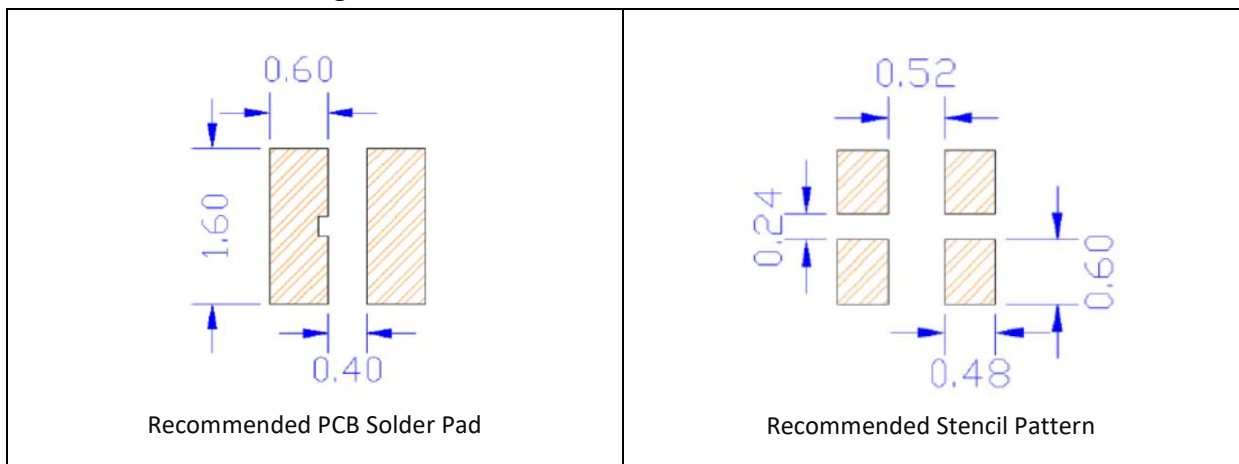
**OUTLINE DIMENSION:**

Package Dimension:



1. All dimensions are in millimetre (mm).
2. Tolerance  $\pm 0.13$ mm, unless otherwise noted.

Recommended Soldering Pad Dimension:



1. Dimensions are in millimetre (mm).
2. Tolerance  $\pm 0.12$ mm with angle tolerance  $\pm 0.5^\circ$ .

**BINNING GROUPS:**


---

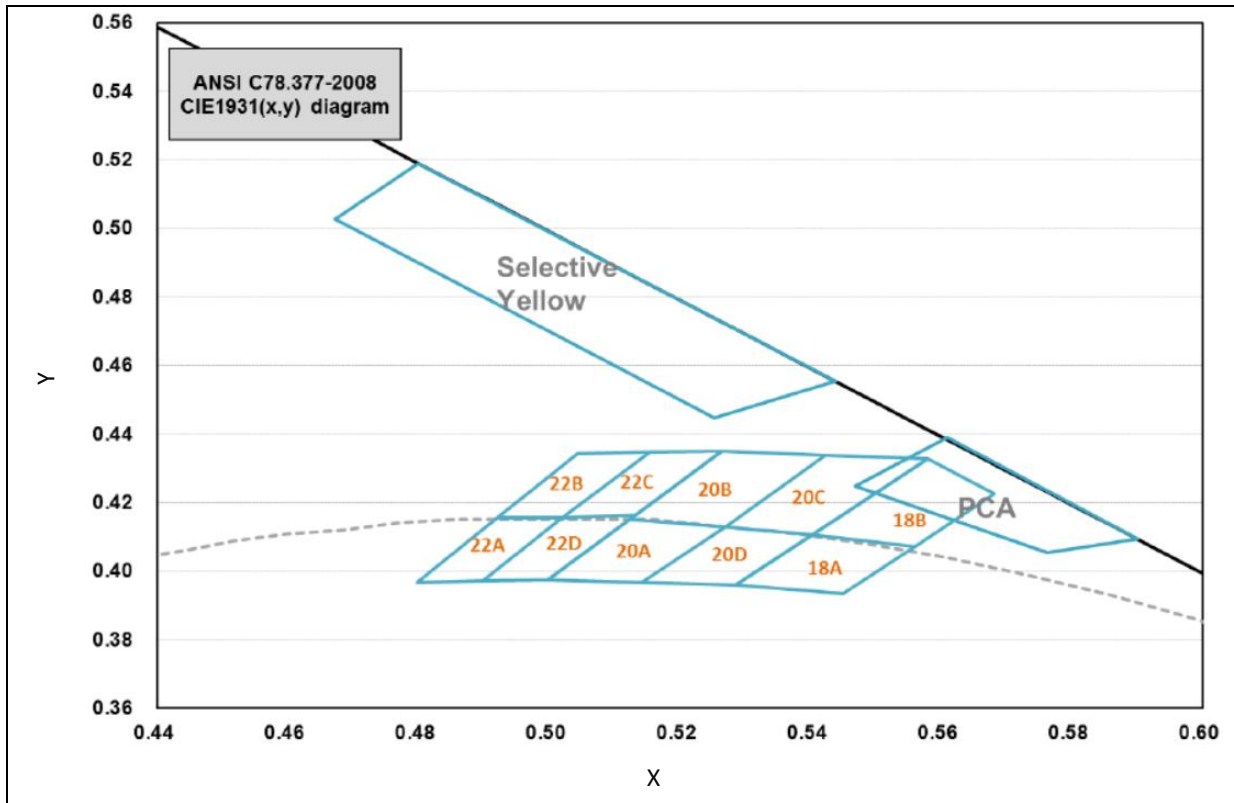
 Forward Voltage Classifications ( $I_F = 700\text{mA}$ ):

Code	Min.	Max.	Unit
M9	2.8	3.0	V
MA	3.0	3.2	
MB	3.2	3.4	

 Luminous Flux Classifications ( $I_F = 700\text{mA}$ ):

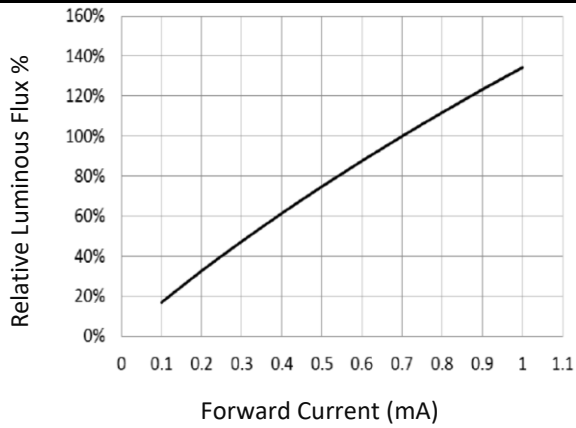
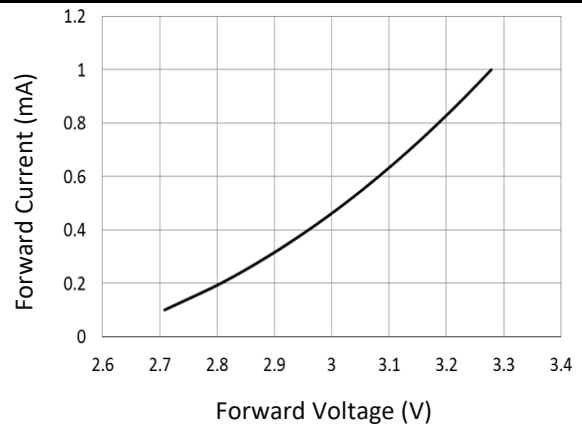
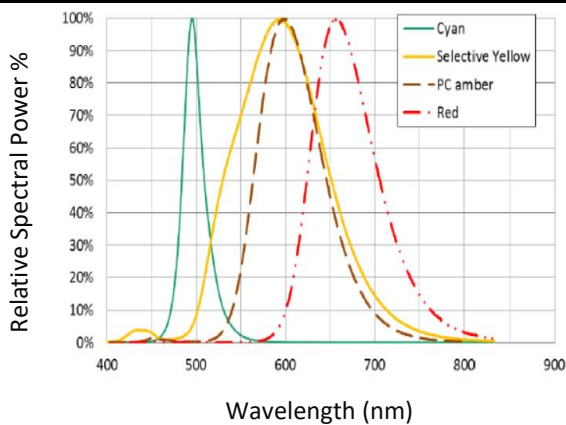
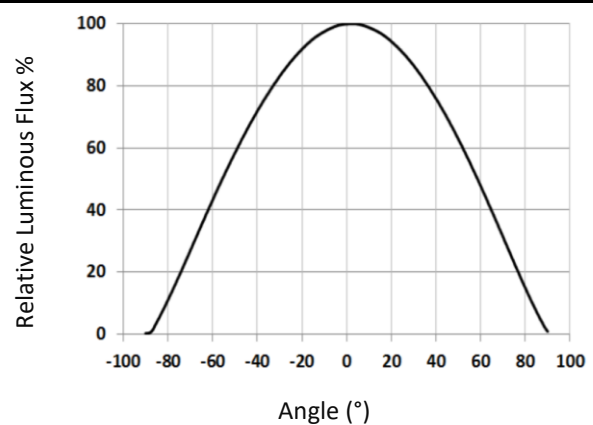
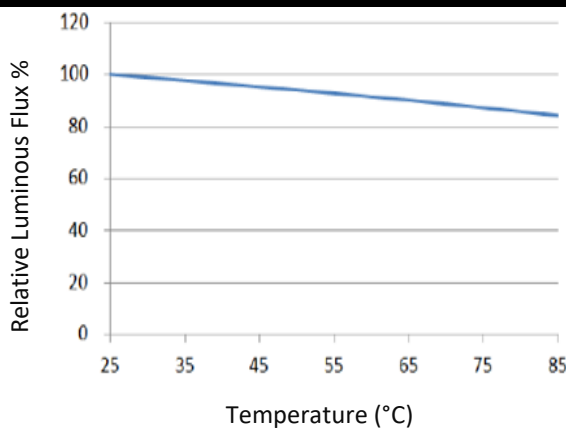
Code	Min.	Max.	Unit
E13	250	270	lm
E14	270	290	

## CIE CHROMATICITY DIAGRAM:



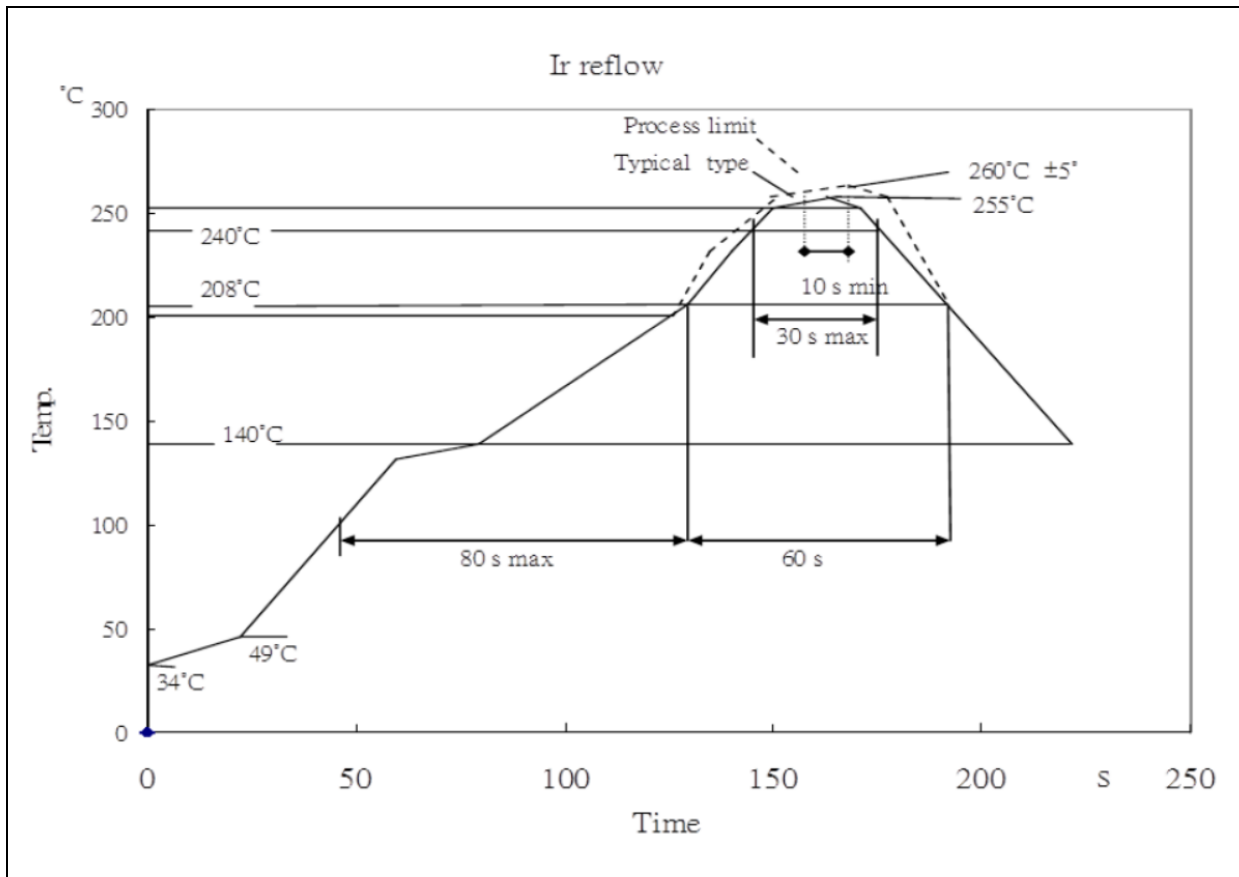
Chromaticity Coordinates Classifications ( $I_F = 700\text{mA}$ ):

	1		2		3		4	
	X	Y	X	Y	X	Y	X	Y
SY	0.5438	0.4554	0.5253	0.4447	0.4673	0.5028	0.4799	0.5190

**ELECTRO-OPTICAL CHARACTERISTICS:**
**Relative Luminous Flux v.s. Forward Current**

**Forward Current v.s. Forward Voltage**

**Relative Spectral Power v.s. Wavelength**

**Directive Radiation**

**Forward Current Derating Curve**


## RECOMMENDED SOLDERING PROFILE:

Reflow Lead-free Solder:

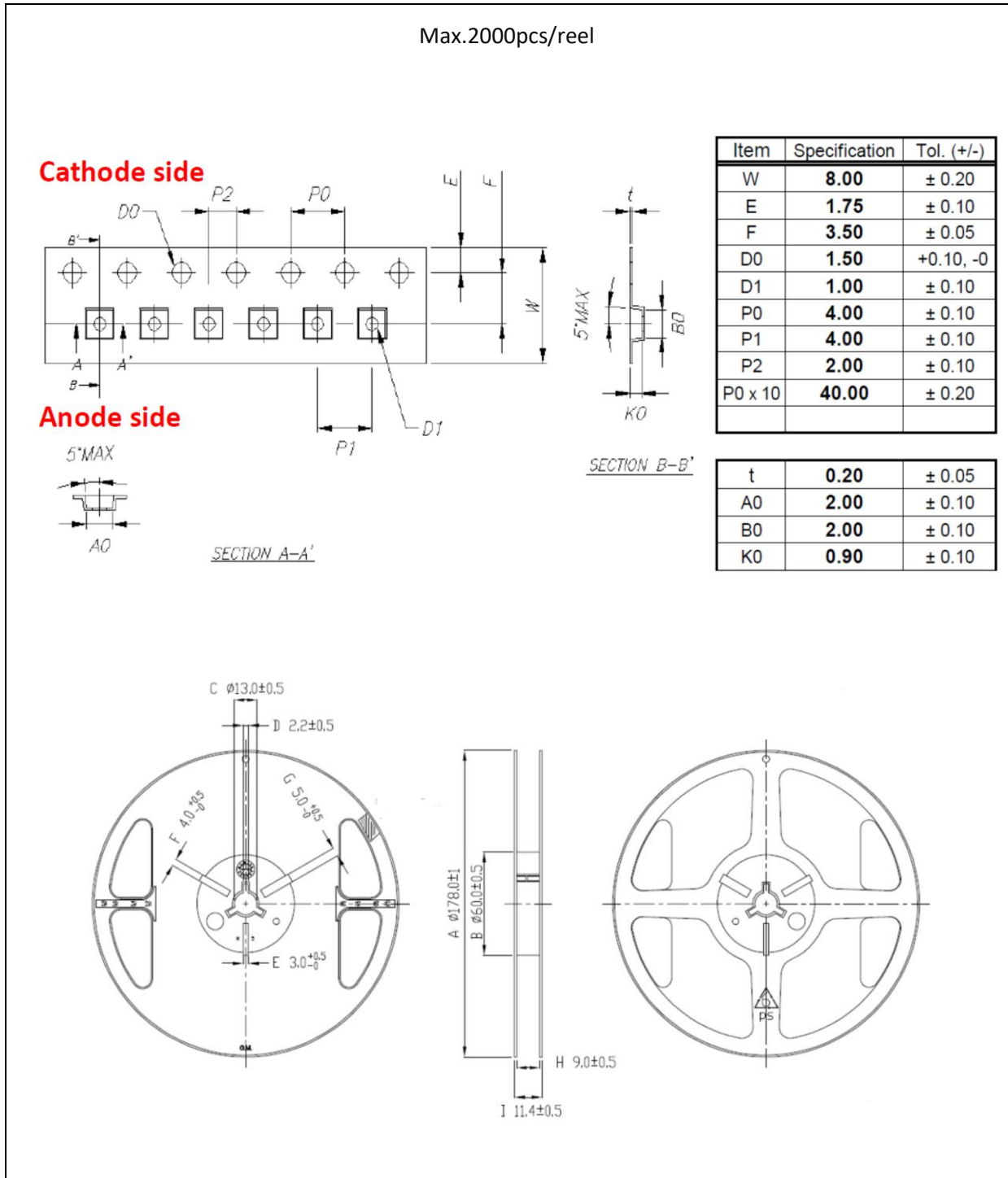


Note:

1. Maxima reflow soldering: 1 time.
2. The recommended reflow temperature is 240°C. The maximum soldering temperature should be limited to 260°C.
3. Before, during, and after soldering, should not apply stress on the components and PCB board.

**PACKING SPECIFICATION:**

Reel Dimension:





## PRECAUTIONS OF USE:

---

### Storage:

It is recommended to store the products in the following conditions:

- Humidity: 60% R.H. Max.
- Temperature: 5°C~30°C (41°F ~86°F).

Shelf life in sealed bag: 12 months at 5°C~30°C and <60% R.H.

Once the package is opened, the products should be used within a week. Otherwise, they should be kept in a damp-proof box with desiccating agent <10% R.H. and apply baking before use.

### Baking:

It is recommended to bake the LED before soldering if the pack has been unsealed for longer than 24hrs. The suggested baking conditions are as followings:

- 60±3°C x 24hrs and <5%RH, taped / reel package.

It's normal to see slight color fading of carrier (light yellow) after baking in process.

### Testing Circuit:



Must apply resistor(s) for protection (over current proof).

### Cleaning:

Use alcohol-based cleaning solvents such as isopropyl alcohol to clean the LED carrier / package. Avoid putting any stress force directly on to the LED lens.

### ESD (Electrostatic Discharge):

Static Electricity or power surge will damage the LED. Use of a conductive wrist band or anti-electrostatic glove is recommended when handling the LED all time. All devices, equipment, machinery, work tables, and storage racks must be properly grounded.

In the events of manual working in process, make sure the devices are well protected from ESD at any time.

**REVISION RECORD:**

---

Version	Date	Summary of Revision
A1.0	22/03/2021	Datasheet set-up.