



**BRIGHTTEK**  
**BRIGHTTEK (EUROPE) LIMITED**

*Brighten up The World With LED!*



ISO/TS 16949:2009



BS EN ISO 14001:2004



QC 080000 IECQ HSPM

## PRODUCT DATASHEET



- ▶ DC Input Photo Coupler
- ▶ SMD8
- ▶ Darlington High Speed Transistor

Release Date: 08 June 2025 Version: A00

# 6N139(S)(T1)-GV



## 6N139(S) Series



### DESCRIPTION:

The 6N139(S) series combine an AlGaAs infrared emitting diode as the emitter which is optically coupled to a silicon high speed photo Darlington transistor in a plastic DIP8 package with SMD8 lead forming option.

A separate design between photodiode and Darlington transistor reduces the base-collector capacitance of the input transistor which improves the speed by several orders of magnitude over conventional phototransistor opto-couplers.

### APPLICATIONS:

- Low current line receivers
- Current loop receivers
- Out interface to CMOS-LSTTL-TTL
- Pulse transformer replacement
- Computer-peripheral interface

### FEATURES:

- High isolation 5000Vrms
- DC input with transistor output
- Operating temperature range -55°C to +100°C
- MSL class 1
- Regulatory Approvals:
  - UL - UL1577
  - VDE - EN60747-5-5 (VDE0884-5)
  - CQC - GB4943.1, GB8898
  - cUL - CSA Component Acceptance Service Notice 5A
- Packing: 1000pcs/reel



Partner with: LIGHTNING

**NAMING & ORDERING INFORMATION:**


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Naming Information:

<b>6N139 (S) (T1)- G V</b>	
<b>6N139</b>	Part Number
<b>S</b>	Lead Form Option: SMD8
<b>T1</b>	Selection: Tape and Reel Option (T1(default)/T2)
<b>G</b>	Green Option
<b>V</b>	VDE Option

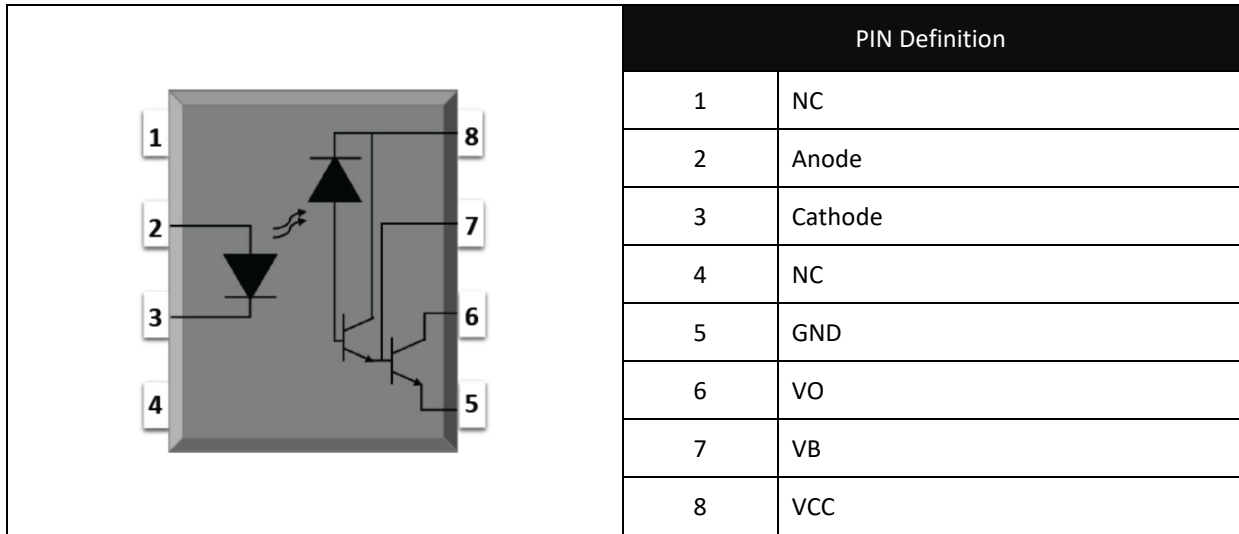
Ordering Information:

<b>6N139(S)(T1)-GV</b>
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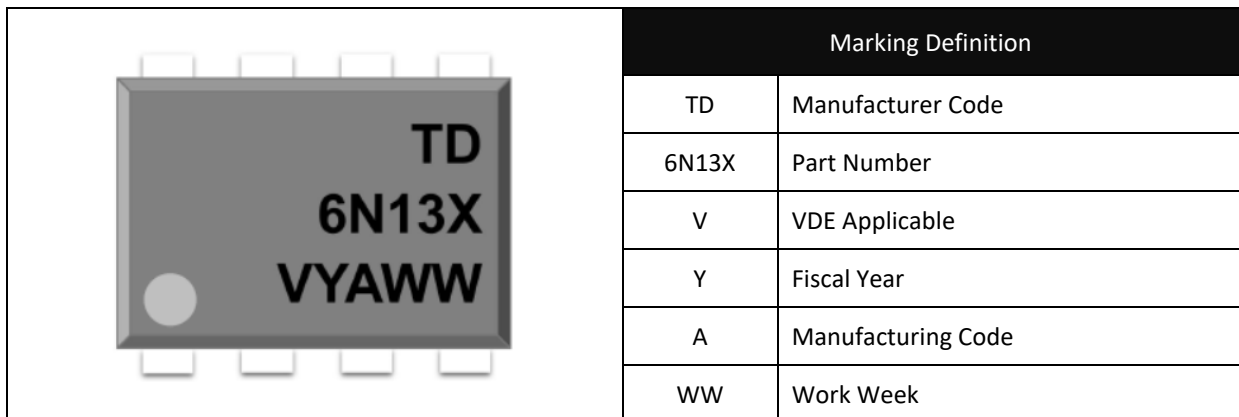
Version No.	Original Release Date
Rev: A00	29/08/2024

## SCHEMATIC DIAGRAM & MARKING:

Schematic Diagram:



Marking Information:



Labelling Information:




Part No.: XXXXXXXXXXXX      Bin Code: X



Lot No.: XXXXXXXX

Date Code: xxxx

QTY: XXX PCS




MSL: 1

Made in Quanzhou Fujian







This product is manufactured, tested, and packed by



for more details, please visit [www.tdled.com](http://www.tdled.com)

## ABSOLUTE CHARACTERISTICS:

### Absolute Maximum Ratings:

Parameter	Symbol	Ratings	Unit
INPUT			
Forward Current	$I_F$	25	mA
Peak Forward Current	$I_{FP}$	50 * <sup>1</sup>	mA
Peak Transient Current	$I_{F(trans)}$	1 * <sup>2</sup>	A
Reverse Voltage	$V_R$	5	V
Input Power Dissipation	$P_I$	100	mW
OUTPUT			
Supply Voltage	$V_{CC}$	-0.5~18	V
Output Voltage	$V_O$	-0.5~18	V
Output Current	$I_O$	60	mA
Emitter-Base Reverse Voltage	$V_{EBR}$	0.5	V
Output Power Dissipation	$P_O$	100	mW
COMMON			
Total Power Dissipation	$P_{tot}$	200	mW
Isolation Voltage	$V_{iso}$	5000 * <sup>3</sup>	V <sub>rms</sub>
Operating Temperature	$T_{opr}$	-55~+100	°C
Storage Temperature	$T_{stg}$	-55~+150	°C
Soldering Temperature	$T_{sol}$	260 * <sup>4</sup>	°C

\*1. 50% duty, 1ms P.W.

\*2.  $\leq 1\mu s$  P.W, 300pps.

\*3. AC for 1 minute, R.H.=40~60%.

\*4. For 10 seconds.

## ELECTRICAL CHARACTERISTICS:

### Electrical Optical Characteristics:

Parameter	Symbol	Values			Unit	Test Condition
		Min.	Typ.	Max.		
INPUT (at T <sub>a</sub> =0 to 70°C, unless specified otherwise)						
Forward Voltage	V <sub>F</sub>	---	1.28	1.7	V	I <sub>F</sub> =1.6mA
Reverse Current	I <sub>R</sub>	---	---	10	μA	V <sub>R</sub> =5V
Input Capacitance	C <sub>IN</sub>	---	60	---	pF	V=0, f=1MHz
OUTPUT (at T <sub>a</sub> =0 to 70°C, unless specified otherwise)						
High Level Supply Current	I <sub>CCH</sub>	---	0.05	10	μA	I <sub>F</sub> =0mA, V <sub>O</sub> =OPEN V <sub>CC</sub> =18V
Low Level Supply Current	I <sub>CCL</sub>	---	0.6	1.5	mA	I <sub>F</sub> =1.6mA V <sub>O</sub> =OPEN V <sub>CC</sub> =18V
Logic High Output Current	I <sub>OH</sub>	---	---	250	μA	I <sub>F</sub> =0mA V <sub>O</sub> =V <sub>CC</sub> =18V
TRANSFER CHARACTERISTICS (at T <sub>a</sub> =0 to 70°C, unless specified otherwise)						
Current Transfer Ratio	CTR	400	2500	---	%	I <sub>F</sub> =0.5mA V <sub>O</sub> =0.4V V <sub>CC</sub> =4.5V
		500	2600	---		I <sub>F</sub> =1.6mA V <sub>O</sub> =0.4V V <sub>CC</sub> =4.5V
Logic Low Output Voltage	V <sub>OL</sub>	---	0.04	0.4	V	I <sub>F</sub> =0.5mA, I <sub>O</sub> = 2mA, V <sub>CC</sub> =4.5V
		---	0.07	0.4		I <sub>F</sub> =1.6mA, I <sub>O</sub> = 8mA, V <sub>CC</sub> =4.5V
		---	0.11	0.4		I <sub>F</sub> =5mA, I <sub>O</sub> = 15mA, V <sub>CC</sub> =4.5V
		---	0.15	0.4		I <sub>F</sub> =12mA, I <sub>O</sub> = 24mA, V <sub>CC</sub> =4.5V
Isolation Resistance	R <sub>iso</sub>	10 <sup>12</sup>	10 <sup>14</sup>	---	Ω	DC500V 40~60% R.H.
Floating Capacitance	C <sub>IO</sub>	---	0.3	1	pF	V=0, f=1MHz

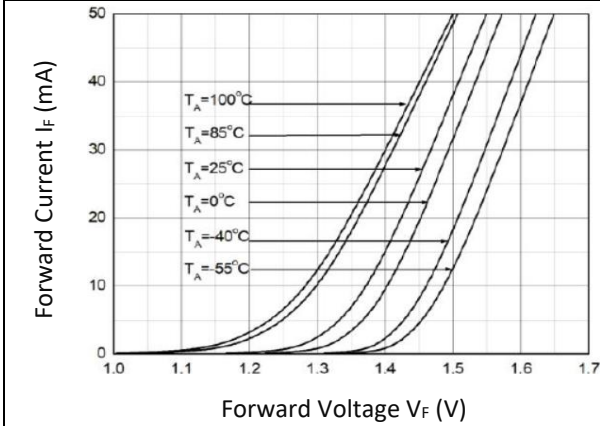
## ELECTRICAL CHARACTERISTICS:

### Electrical Optical Characteristics:

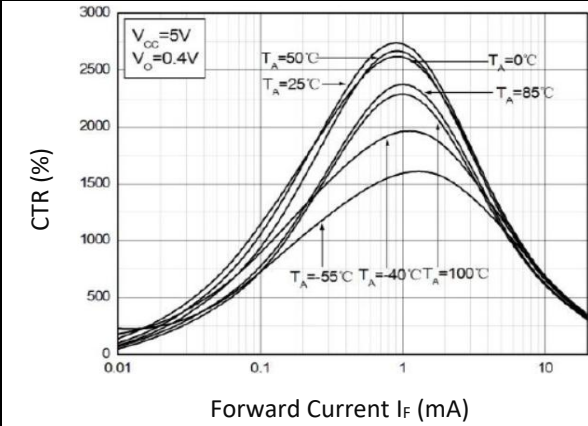
Parameter	Symbol	Values			Unit	Test Condition
		Min.	Typ.	Max.		
SWITCHING CHARACTERISTICS (at $T_a=0$ to $70^\circ\text{C}$ , $V_{CC}=5\text{V}$ , unless specified otherwise)						
Propagation Delay Time to Logic Low	$T_{PHL}$	---	5	25	$\mu\text{s}$	$I_F=0.5\text{mA}$ $R_L=4.7\text{k}\Omega$ $T_a=25^\circ\text{C}$
		---	---	30		$I_F=0.5\text{mA}$ $R_L=4.7\text{k}\Omega$
		---	0.2	1		$I_F=12\text{mA}$ $R_L=270\Omega$ $T_a=25^\circ\text{C}$
		---	---	2		$I_F=12\text{mA}$ $R_L=270\Omega$
Propagation Delay Time to Logic High	$T_{PLH}$	---	22	60	$\mu\text{s}$	$I_F=0.5\text{mA}$ $R_L=4.7\text{k}\Omega$ $T_a=25^\circ\text{C}$
		---	---	90		$I_F=0.5\text{mA}$ $R_L=4.7\text{k}\Omega$
		---	2.1	7		$I_F=12\text{mA}$ $R_L=270\Omega$ $T_a=25^\circ\text{C}$
		---	---	10		$I_F=12\text{mA}$ $R_L=270\Omega$
Common Mode Transient Immunity at Logic High	$CM_H$	1000	---	---	$\text{V}/\mu\text{s}$	$I_F=0\text{mA}$ $V_{CM}=10\text{V}_{pp}$ $R_L=2.2\text{k}\Omega$ $T_a=25^\circ\text{C}$
Common Mode Transient Immunity at Logic Low	$CM_L$	1000	---	---	$\text{V}/\mu\text{s}$	$I_F=1.6\text{mA}$ $V_{CM}=10\text{V}_{pp}$ $R_L=2.2\text{k}\Omega$ $T_a=25^\circ\text{C}$

## CHARACTERISTIC CURVES:

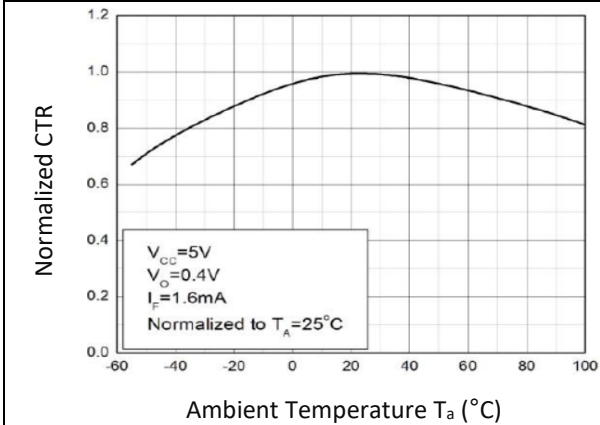
Forward Current  $I_F$  v.s. Forward Voltage



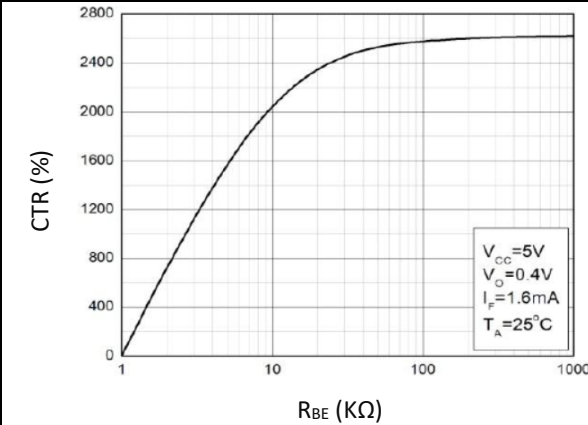
Current Transfer Ratio v.s. Forward Current



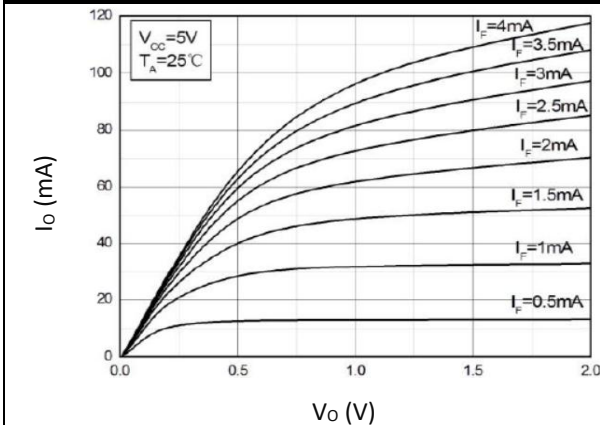
Normalized Current Transfer Ratio v.s. Ambient Temperature



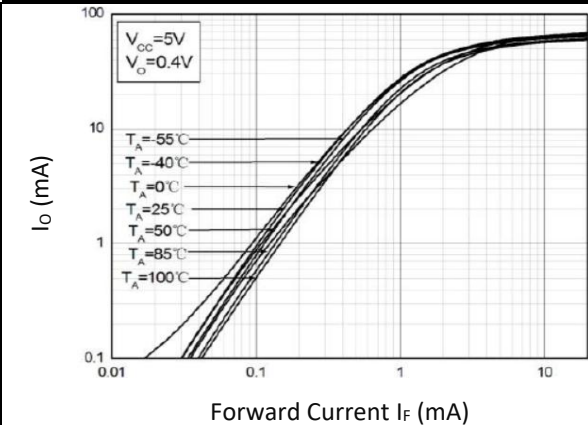
Current Transfer Ratio v.s. Base-Emitter Resistance



Low Level Output Current v.s. Output Voltage

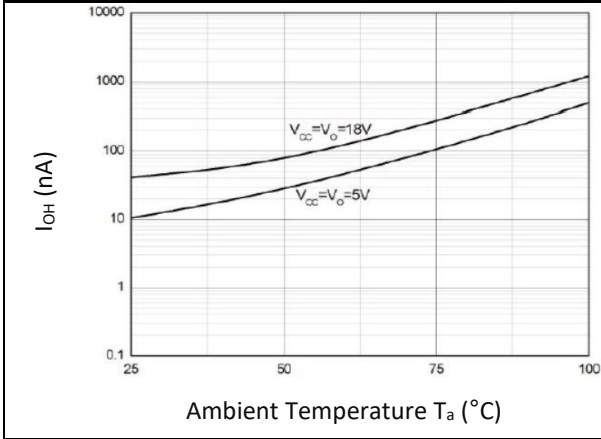


Low Level Output Current v.s. Forward Current

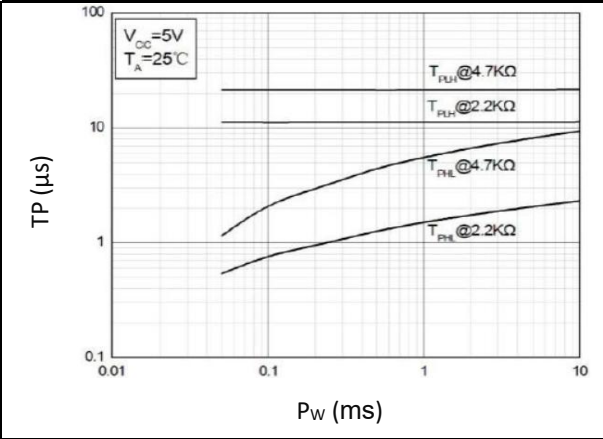


### CHARACTERISTIC CURVES:

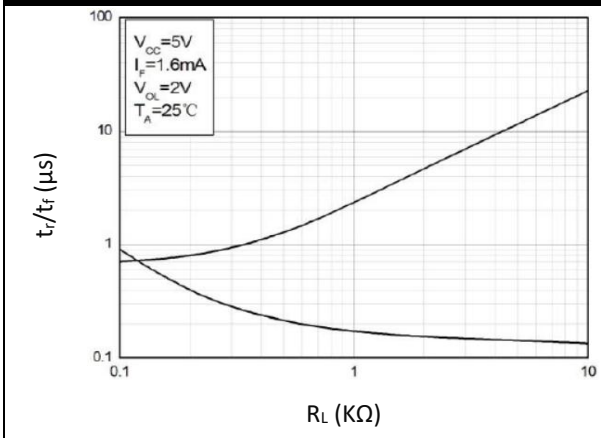
High Level Output Current v.s. Ambient Temperature



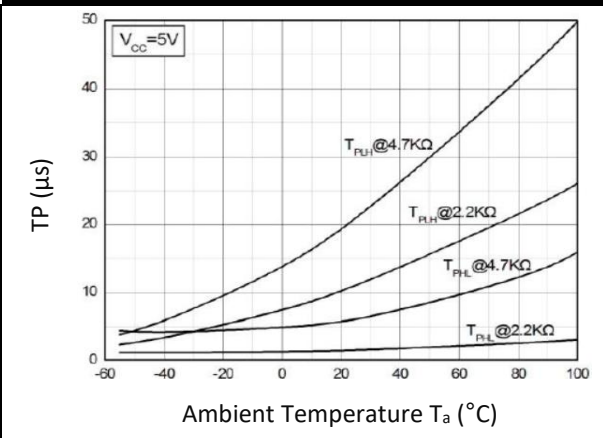
Propagation Delay v.s. Pulse Width



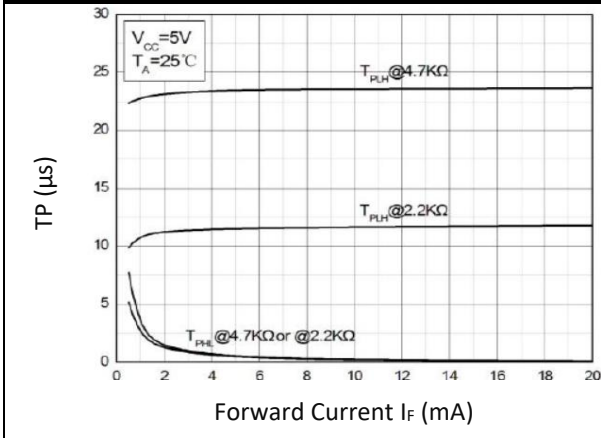
Rise and Fall Time v.s. Load Resistance



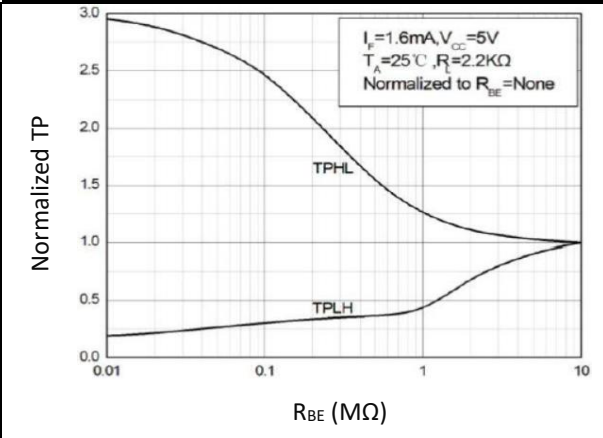
Propagation Delay v.s. Ambient Temperature



Propagation Delay v.s. Forward Current



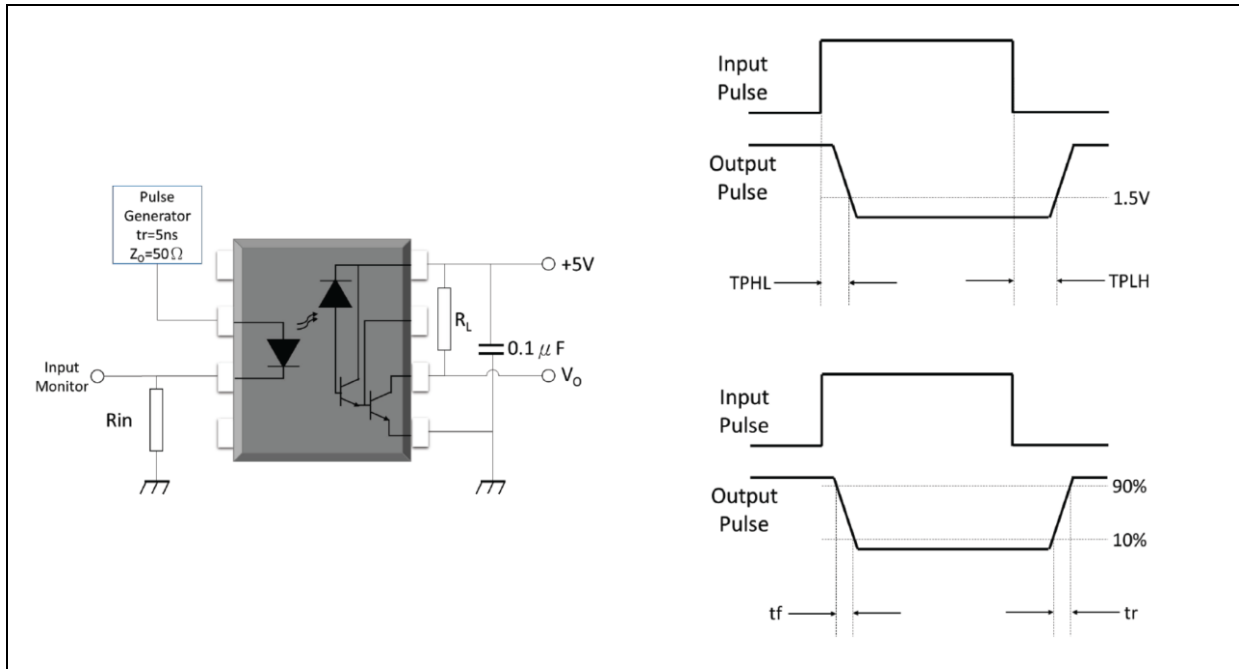
Propagation Delay v.s. Base-Emitter Resistance



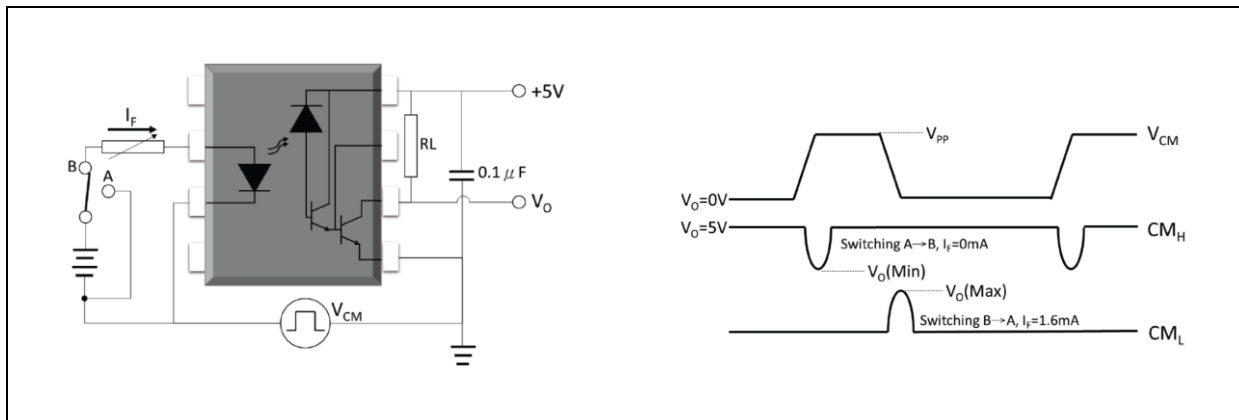


## TEST CIRCUIT:

Test Circuit and Waveforms for TPHL, TPLH,  $t_r$ ,  $t_f$ :

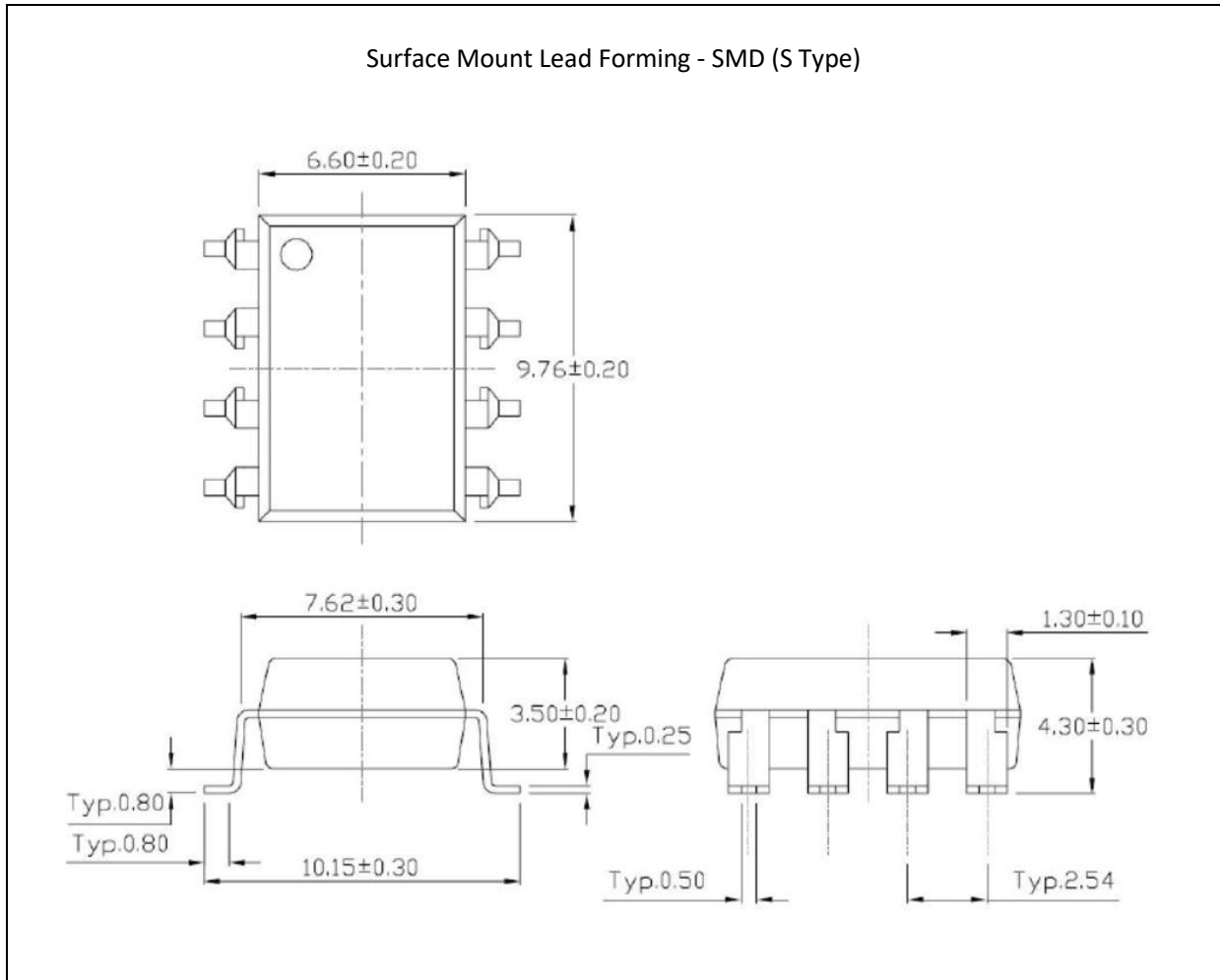


Test Circuit and Waveforms for Common Mode Transient Immunity:



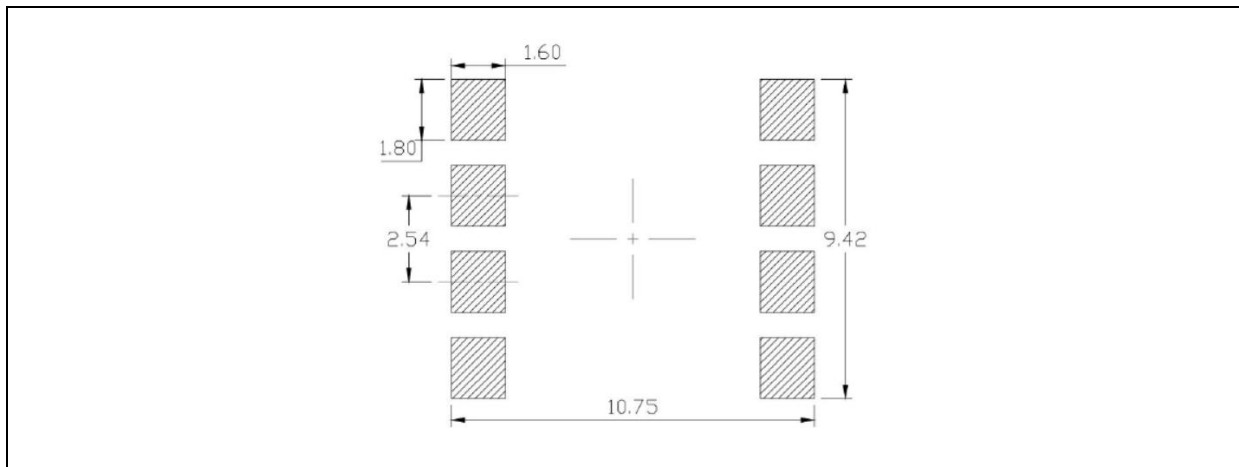
## OUTLINE DIMENSION:

Package Dimension:



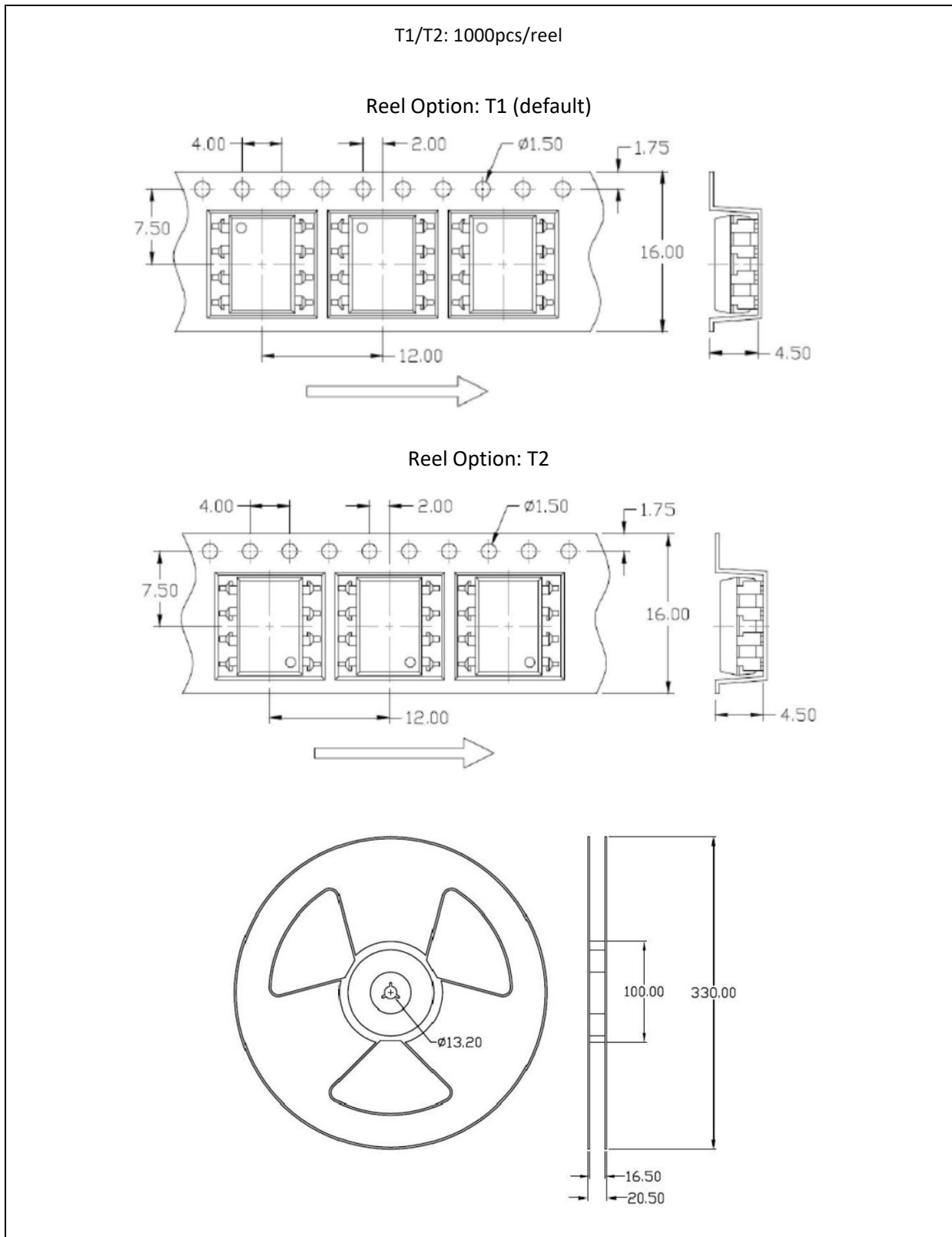
1. All dimensions are in millimetre (mm).

Recommended Soldering Mask:



**PACKING SPECIFICATION:**

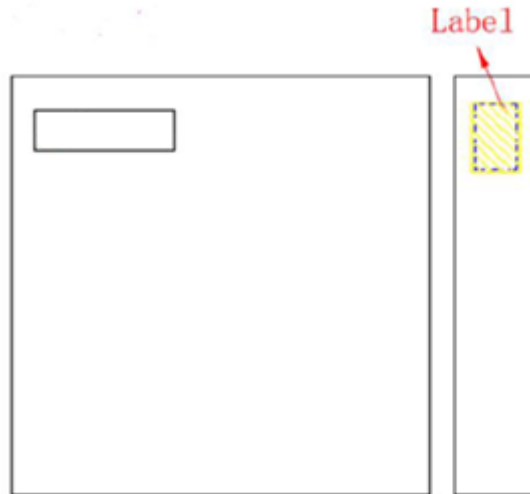
Reel Dimension:



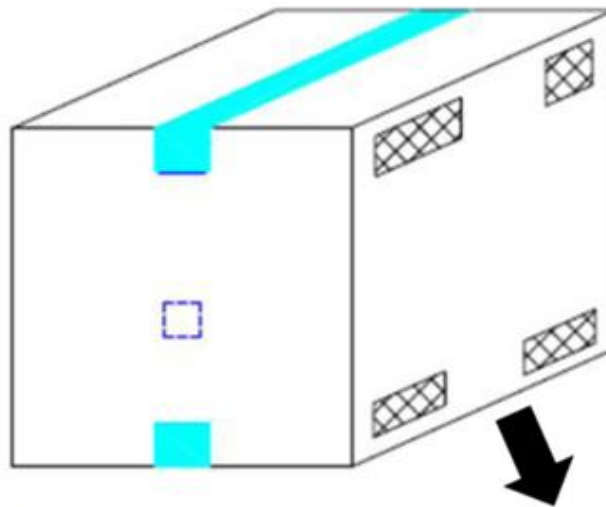
## PACKING SPECIFICATION:

Box Dimension:

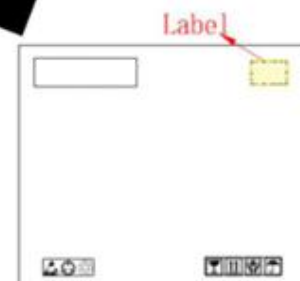
T1/T2: 3 reels (3Kpcs)/inner box, 5 inner boxes (15Kpcs)/carton



- L x W x H = 36cm x 36cm x 6.9cm

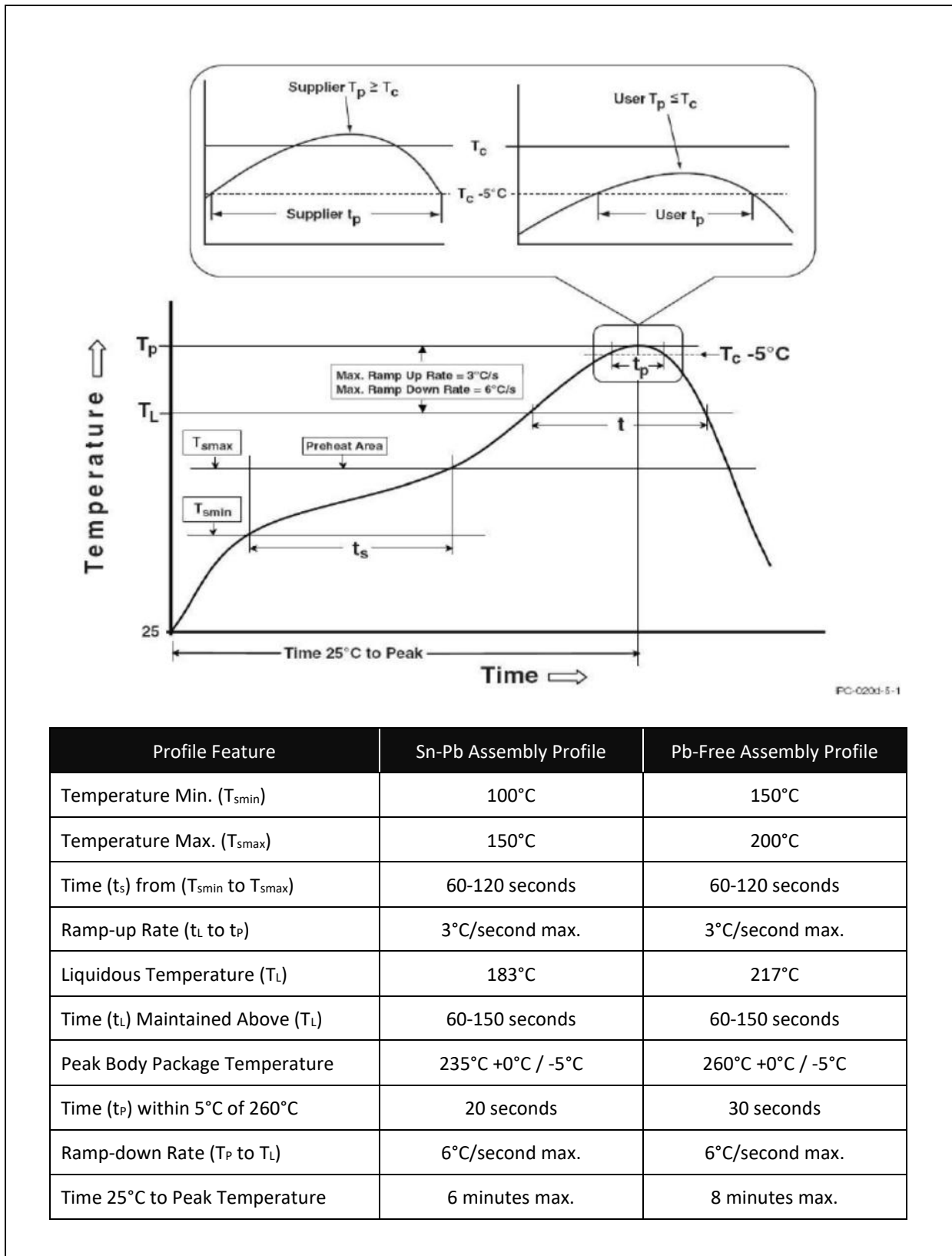


- L x W x H = 45cm x 38cm x 38cm



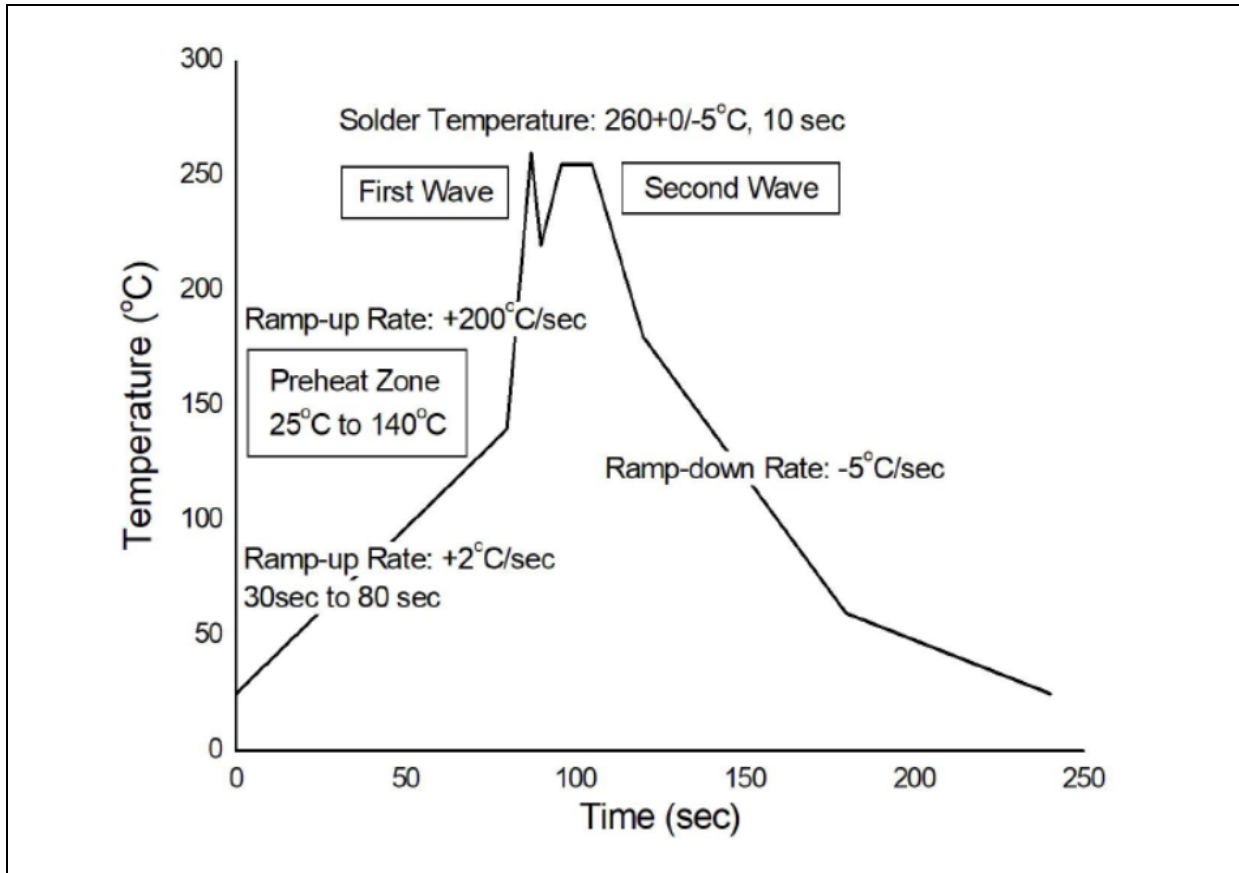
## RECOMMENDED SOLDERING PROFILE:

Reflow Information:



## RECOMMENDED SOLDERING PROFILE:

Wave Soldering (JESD22-A111 Compliant):



Hand Soldering:

Soldering Temperature	380±5°C
Soldering Time	3 sec max.

Note:

- One time soldering is recommended for all soldering methods.
- Do not solder more than three times for IR reflow soldering.