



Specification for Approval

Customer: _____

Model Name: _____

Supplier Approval			Customer approval
R&D Designed	R&D Approved	QC Approved	
<i>Peter</i>	<i>Peng Jun</i>		



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1. GENERAL DESCRIPTION

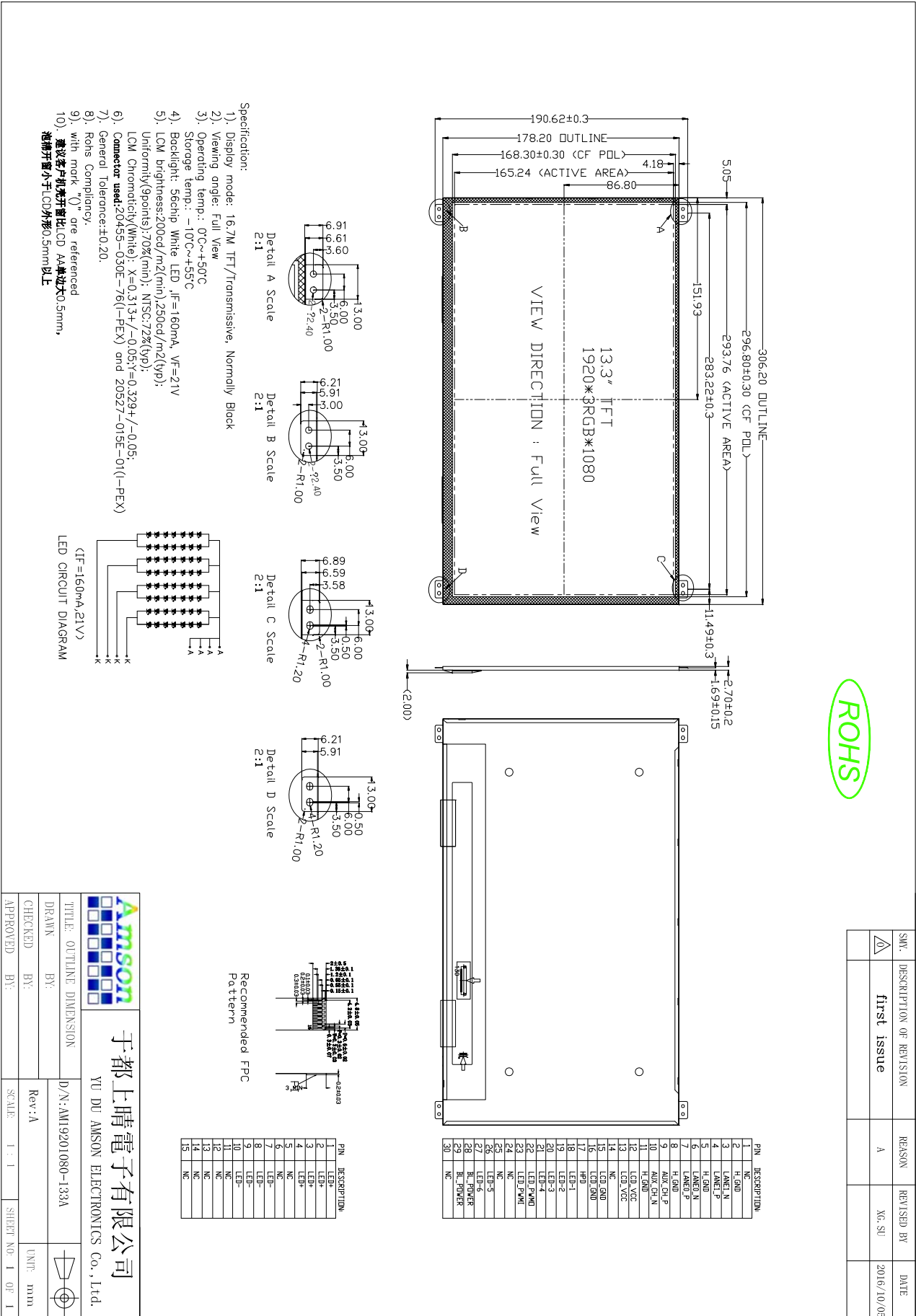
1.1 DESCRIPTION

M133FDDI30-01A is a color active matrix thin film transistor (TFT) IPS liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. It is composed of a TFT LCD panel, Driver IC ,PCBA and Backlight.

1.2 FEATURES:

No.	Item	Specification	Unit
1	Panel Size	13.3"	inch
2	Number of Pixels	1920×RGB (3) ×1080	pixels
3	Active Area	293.76(H)x 165.24(V)	mm
4	Pixel Pitch	0.153 (H)×0.153(V)	mm
5	Outline Dimension	306.2(W)×178.2(H)×2.7(D)	mm
6	Number of Colors	16.7M	-
7	Display Mode	Normally Black	-
8	Viewing Direction	ALL	-
9	Display Format	RGB vertical stripe	-
10	Luminance (cd/m ²)	250 (TYP.)	nit
11	Contrast Ratio	700(min.)	
12	Surface Treatment	Anti Glare	-
13	Interface	eDP	-
14	Backlight	White LED	-
15	Operation Temperature	0~50	°C
16	Storage Temperature	-10~55	°C
17	Polarizer Type	AG:Anti Glare(2H,3H)	-
		HC:Hard Coating	-

2. MECHANICAL SPECIFICATION



3. PIN DESCRIPTION

No.	Symbol	I/O	Function	Remark
1	NC	-	No connection	
2	H_GND	P	Ground	
3	LAN1_N	I	Complement Signal Link Lane 1	
4	LAN1_P	I	True Signal Link Lane 1	
5	H_GND	P	Ground	
6	LAN0_N	I	Complement Signal Link Lane 0	
7	LAN0_P	I	True Signal Link Lane 0	
8	H_GND	P	Ground	
9	AUX_CH_P	I	True Signal Auxiliary CH	
10	AUX_CH_N	I	Complement Signal Auxiliary CH	
11	H_GND	P	Ground	
12	LCD_VCC	P	Power Supply 3.3V(typ)	
13	LCD_VCC	P	Power Supply 3.3V(typ)	
14	NC	-	No connection	
15	LCD_GND	P	Ground	
16	LCD_GND	P	Ground	
17	HPD	O	HPD Singnal output pin	
18	LED-1	P	BL Power Ground	
19	LED-2	P	BL Power Ground	
20	LED-3	P	BL Power Ground	
21	LED-4	P	BL Power Ground	
22	LED_PWMO	O	BL PWM signal output	
23	LED_PWM I	I	BL PWM signal input	
24	NC	-	No connection	
25	NC	-	No connection	
26	LED-5	P	BL Power Ground	
27	LED-6	P	BL Power Ground	
28	BL_POWER	P	BL Power Supply(21V/160mA)	
29	BL_POWER	P	BL Power Supply(21V/160mA)	
30	NC	-	No connection	

4. ELECTRICAL CHARACTERISTICS

4.1 ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
Logic power supply	LCD_VCC	-0.3	3.3	4.2	V	

4.2 OPERATING CONDITIONS

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Logic power supply	LCD_VCC	3.0	3.3	3.6	V	

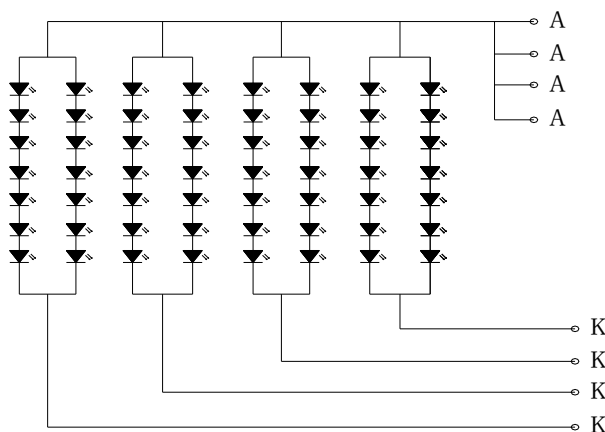
4.3 CURRENT CONSUMPTION

Item	Symbol	Condition	Values			Unit	Remark
			Min.	Typ.	Max.		
Logic power Current	IVCC	VCC=3.3V	500	-	-	mA	

4.4 BACK LIGHT UNIT

Ta=25℃

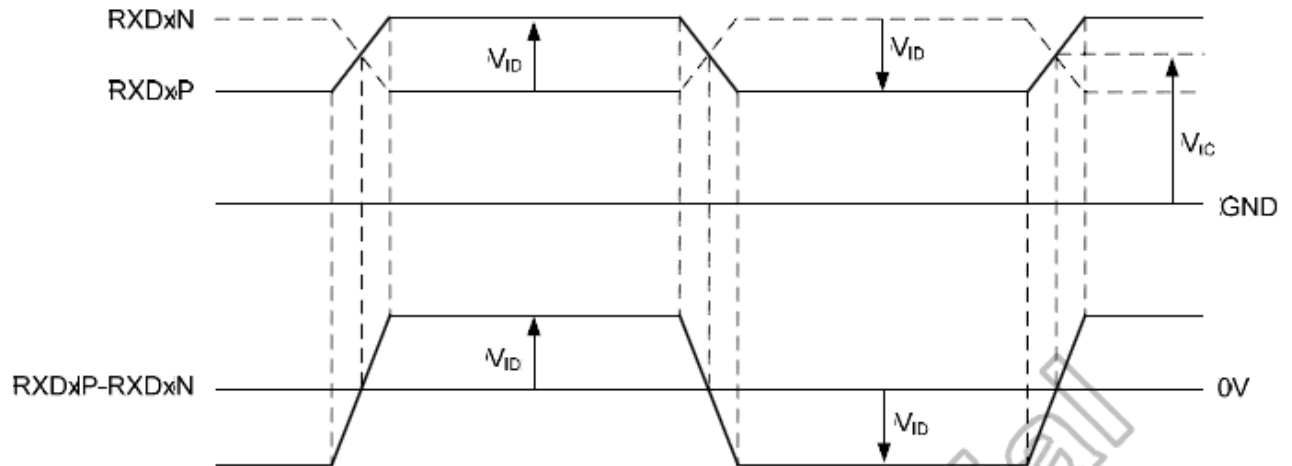
Item	Symbol	Min.	Typ.	Max.	Unit	Remark
LED current	I _{LED}		160		mA	56LEDS
Forward voltage	V _F	20.3	21	23.1	V	I _F =160mA 56LEDS
Reverse current	I _R			50	μA	V _R =10V, 1LED
Luminous tolerance	IV-M	70			75	
Power dissipation	P _d	3400			mW	56LEDS
Peak forward current	I _{FP}	100			mA	1LED
Reverse Voltage	V _R	10			V	1LED


<I_F=160mA, 21V>

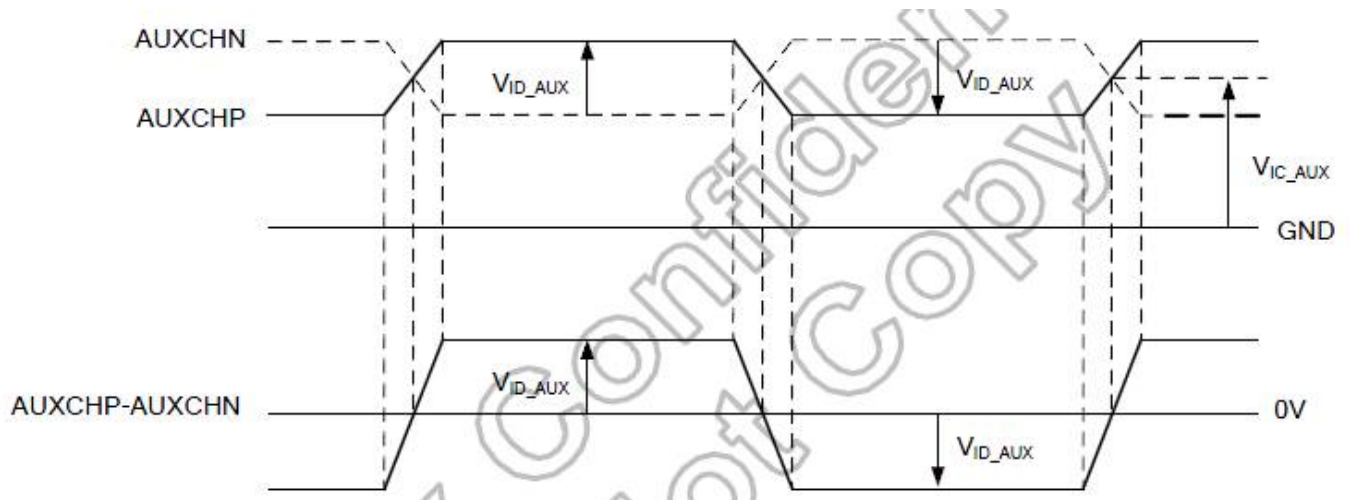
LED CIRCUIT DIAGRAM

5. INPUT SIGNAL TIMING

5.1 SIGNAL TIMING WAVEFORMS OF INTERFACE SIGNAL



Main Link Vid and Vic definition



AUX_CH Vid_aux and Vic_aux definition

5.2 DC CHARACTERISTICS

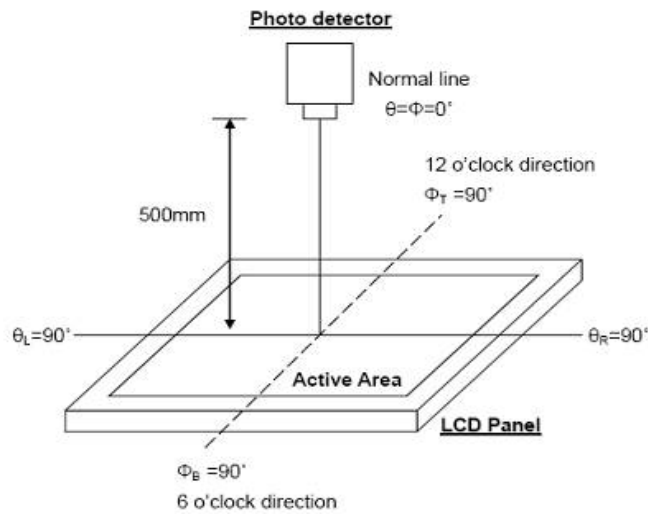
Symbol	Parameter	Condition	Spec.			Unit
			Min.	Typ.	Max.	
CMOS/TTL DC specifications						
V _{IH}	High level input voltage	-	0.7VDDIO	-	VDDIO	V
V _{IL}	Low level input voltage	-	VSSIO	-	0.3VDDIO	V
V _{OH}	High level output voltage	-	0.8VDDIO	-	VDDIO	V
V _{OL}	Low level output voltage	-	VSSIO	-	0.2VDDIO	V
I _{IN}	Input current	-	-10	-	10	μA
R _{PD}	Pull low resistance	AGMODE (Pin 13) PWMI (Pin 14) TEST (Pin 18) CABC_EN (Pin 58) COLOR_EN (Pin 59)	75	150	225	KΩ
DP DC specifications						
V _{IC}	Main link common mode voltage	-	0	-	2.0	V
V _{ID}	Main link swing voltage	2.7 Gbps	±60	-	±600	mV
		1.62 Gbps	±20	-	±600	mV
V _{IC_AUX}	AUX common mode voltage	-	0	-	2.0	V
V _{ID_AUX}	AUX swing voltage	transmitting	±0.195	-	±0.69	V
		receiving	±0.16	-	±0.68	V
mini-LVDS DC specifications						
V _{OD}	Output differential voltage	RL=100Ω (TA=25°C)	±170	±200	±230	mV
V _{OS}	Output offset voltage		0.6	0.8	1.0	V
I _{OD}	Output current		1.7	2	2.3	mA
PWM DC specifications						
V _{LX}	LX pin spike voltage	-	-2	-	3.6	V

6.OPTICAL CHARACTERISTICS

 $T_a = 25^{\circ}\text{C} \pm 2$

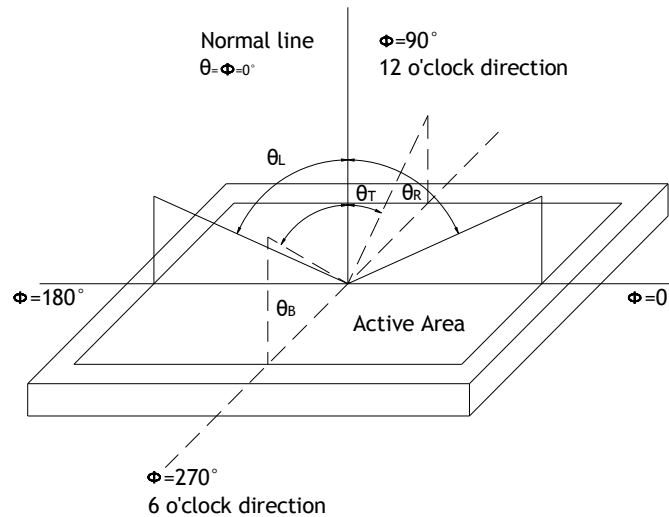
Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio		CR	$\Theta = 0^{\circ}$	700	-	-		Note1 Note4
Luminance		YL		200	250	-	cd/m2	Note1 Note6 Note7
Luminance Uniformity		IV-M		70	-	-	%	
Response Time (Rising + Falling)		T _{RT}	Ta= 25°C $\Theta = 0^{\circ}$	-	10	20	ms	Note1 Note3
Viewing Angle range	Horizontal	ΘL	CR > 10	80	89	-		Note2
		ΘR		80	89	-		
	Vertical	ΘU		80	89	-		
		ΘD		80	89	-		
Color Chromaticity	White	x	$\Theta = 0^{\circ}$	TBD	TBD	TBD		Note1 Note5 Note7
		y		TBD	TBD	TBD		
	Red	x		TBD	TBD	TBD		
		y		TBD	TBD	TBD		
	Green	x		TBD	TBD	TBD		
		y		TBD	TBD	TBD		
	Blue	x		TBD	TBD	TBD		
		y		TBD	TBD	TBD		
NTSC				-	72	-	%	

Note1: Definition of optical measurement system.



Note2: Definition of viewing angle range and measurement system

Viewing angle is measured at the center point of the LCD by CONOSCOPE (ergo-80).



Note3: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.

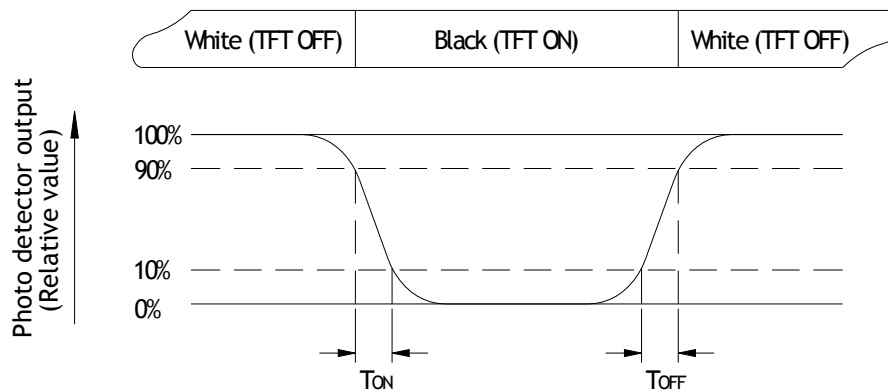


Fig. 6-3 Definition of response time

Note4: Definition of contrast ratio

Luminance measured when LCD on the Whitestate

Contrast ratio(CR)=**Luminance measured when LCD on the Blackstate**

“White state “: The state is that the LCD should drive by V_{white} .

“Black state”: The state is that the LCD should drive by V_{black} .

V_{white} : To be determined V_{black} : To be determined.

Note5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

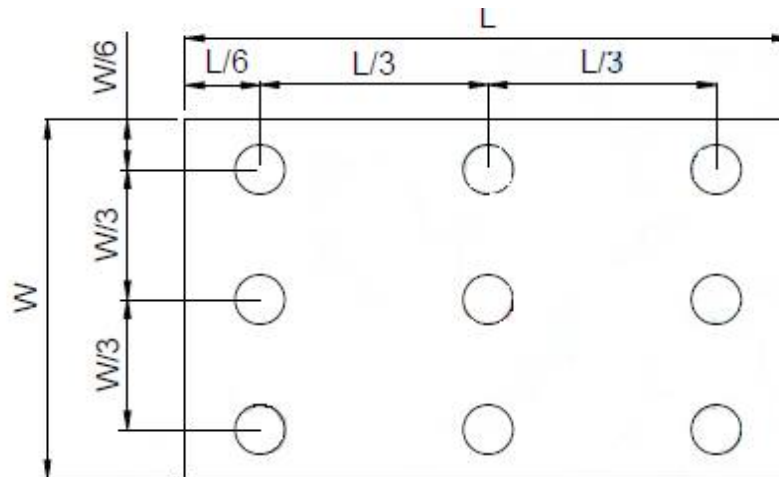
Note6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is $I_L=160\text{mA}$.

Note7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas. Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U) = L_{min} / L_{max}

L----Active area length, W----- Active area width



B_{max} : The measured maximum luminance of all measurement position.

B_{min} : The measured minimum luminance of all measurement position.

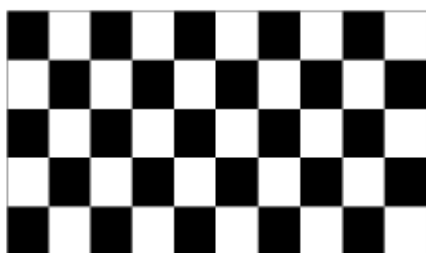
7. RELIABILITY TEST ITEMS

7.1 TEMPERATURE AND HUMIDITY

Test Item	Test Condition	Remark
High Temperature Storage	Ta=55℃; 72hrs	IEC60068-2-1 : 2007 GB2423.2-2008
Low Temperature Storage	Ta=-10℃; 72hrs	IEC60068-2-1 : 2007 GB2423.1-2008
High Temperature Operation	Ta=50℃ , 72Hrs	IEC60068-2-1 : 2007 GB2423.2-2008
Low Temperature Operation	Ta=0℃; 72hrs	IEC60068-2-1 : 2007 GB2423.1-2008
High Temperature High Humidity Operation	Ta=50℃ , 90%RH , 72Hrs(no condensation)	IEC60068-2-78 : 2001 GB/T2423.3-2006
Thermal Shock	-10℃ (0.5h) ~ 55℃ (0.5h) / 10cycles	Start with cold temperature , End with high temperature , IEC60068-2-14:1984,GB2423.22-2002
Image Sticking	25℃ ; 4hrs	Note1

Note1:Condition of image sticking test :25℃±2℃

Operation with test pattern sustained for 4hrs,then change to gray pattern immediately.after5 mins,themura must be disappeared completely



(a) Test Pattern (chess board Pattern)



(b) Gray Pattern

7.2 VIBRATION & SHOCK

Test item	Conditions	Remark
Packing Shock (non-operation)	980m/s ² ,6ms, ±x,y,z 3times for direction	IEC60068-2-27 : 1987 GB/T2423.5-1995
Packing Vibration (non-operation)	Frequency range:10 HZ~50HZ Stroke:1.0mm,sweep:10 HZ ~50HZ x,y,z 2 hours for each direction	IEC60068-2-32 : 1990 GB/T2423.8-1995

7.3 ESD

Test item	Conditions	Remark	
Electro Static Discharge Test (non-operation)	150pF , 330Ω , Contact:±4KV,Air:±8KV	1	IEC61000-4-2 : 2001 GB/T17626.2-2006
	200pF , 0Ω , ±200V contact test	2	

Note: Measure point :

1. LCD glass and metal bezel
2. IF connector pins

8. GENERAL PRECAUTION

8.1 SAFETY

1. Do not swallow any liquid crystal, even if there is no proof that liquid crystal is poisonous.
2. If the LCD panel breaks, be careful not to get liquid crystal to touch your skin.
3. If skin is exposed to liquid crystal, wash the area thoroughly with alcohol or soap.

8.2 STORAGE CONDITIONS

1. Store the panel or module in a dark place where the temperature is $23\pm5^{\circ}\text{C}$ and The humidity is below $50\pm 20\%\text{RH}$.
2. Store in anti-static electricity container.
3. Store in clean environment, free from dust, active gas, and solvent.
4. Do not place the module near organics solvents or corrosive gases.
5. Do not crush, shake, or jolt the module.

8.3 HANDLING PRECAUTIONS

- (1) Avoid static electricity which can damage the CMOS LSI.
- (2) The polarizing plate of the display is very fragile. So, please handle it very carefully.
- (3) Do not give external shock.
- (4) Do not apply excessive force on the surface.
- (5) Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the Surface of plate.
- (6) Do not use ketonics solvent & Aromatic solvent, use with a soft cloth soaked with a cleaning naphtha solvent.
- (7) Do not operate it above the absolute maximum rating.
- (8) Do not remove the panel or frame from the module.
- (9) When the module is assembled, it should be attached to the system firmly, Be careful not to twist and bend the module.
- (10) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining and discoloration may occur.
- (11) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.

8.4 WARRANTY

- (1) The period is within twelve months since the date of shipping out under normal using and storage conditions.
- (2) Do not repaired or modified the LCM. It may cause function to lose efficacy, Xian Chuang does not warrant the LCM.
- (3) All process and material comply ROHS.



9. PACKAGE DRAWING

TBD