Version: B

2021-08-16

# Specification for Approval

Customer:_		
Model Nam	ne:	

Sı	Customer approval		
R&D Designed	R&D Approved	QC Approved	
Peter	Peng Jun		



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## **Revision Record**

REV NO.	REV DATE	CONTENTS	Note
Α	2019-11-20	NEW ISSUE	
В	2021-08-16	MODIFY B/L FPC	

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### 1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by AMSON electronics.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution

### 2. General Information

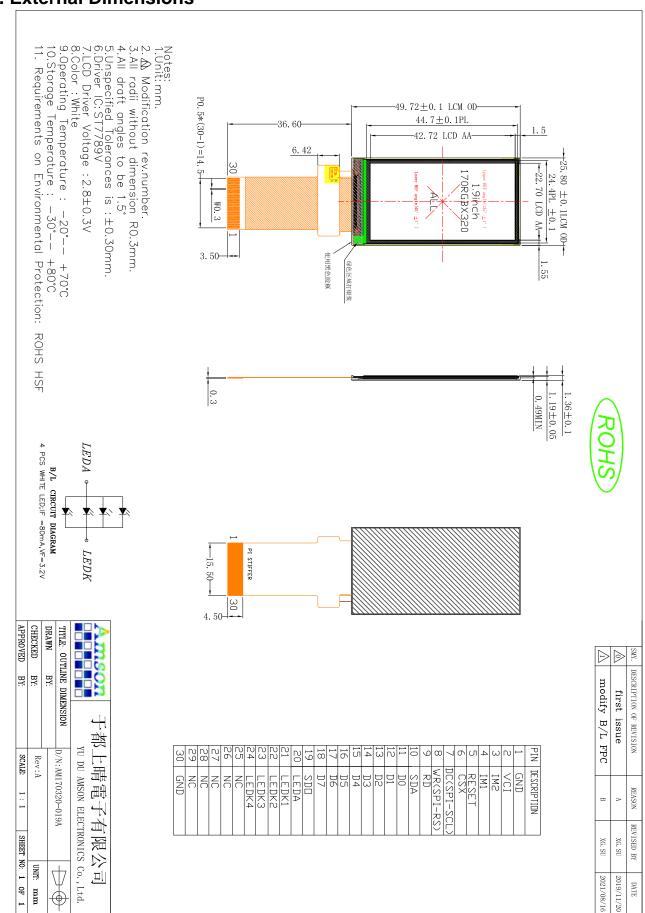
ITEM	STANDARD VALUES	UNITS
LCD type	1.9"TFT	
Dot arrangement	170(RGB)×320	dots
Color filter array	RGB vertical stripe	
Display mode	IPS / Transmissive / Normally Black	-
Gray Scale Inversion Direction	80/80/80	
Eyes Viewing Direction	ALL	
Driver IC	ST7789V	
Module size	25.80(W)×49.72(H)×1.36(T)	mm
Active area	22.70(W)×42.72(H)	mm
Interface	4 line SPI / 8-bit MCU interface	
Operating temperature	-20 ~ <b>+7</b> 0	°C
Storage temperature	-30 ~ +80	°C
Back Light	4 White LED	



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#### 3. External Dimensions





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4. Interface Description

PIN NO.	PIN NAME	DESCRIPTION
1	GND	Ground
2	VCI	Power Supply 2.8V Voltage
3	IM2	when IM1=0, IM2=0, 8080-8bit;
4	IM1	when IM1=1, IM2=1, 4-line SPI serial
5	RESET	LCM Reset input signal
6	CSX	Input pin for chip selection signal
7	DC(SPI-SCL)	When connecting to an 8080-series microprocessor, this pin receives the data/command selection pin .This pin is used to be serial interface clock in 4-line serial interface
8	WR(SPI-RS)	When connecting to an 8080-series microprocessor, this pin receives the write signal.  Display data/command selection pin in 4-line serial interface.
9	RD	When connecting to an 8080-series microprocessor, this pin receives the Read signal. Read operation is initiated when this pin is pulled LOW and the chip is selected. When serial interface is selected, this pin must be connected to Ground.
10	SDA	SPI interface input/output pin. The data is latched on the rising edge of the SCL signal.
11-18	D0-D7	MCU parallel interface data bus.
19	SDO	SPI interface output pin.
20	LEDA	LED Anode
21	LEDK	LED Cathode
22	LEDK	LED Cathode
23	LEDK	LED Cathode
24	LEDK	LED Cathode
25-29	NC	No Connect
30	GND	Ground



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5. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit
Power Supply Voltage	VCI	1.6	3.3	V
Operating Temperature	Тор	-20	70	°C
Storage Temperature	Тѕт	-30	80	°C
Storage Humidity	HD	20	90	%RH

#### 6. DC Characteristics

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Power Supply Voltage	VCI	2.6	2.8	3.3	V	-
Supply Current	Icc	-	-	25	mA	VCI=2.8V.Ta=25°C
Input High Voltage	V <sub>IH</sub>	0.8IOVCC	-	IOVCC	V	-
Input Low Voltage	$V_{IL}$	GND	-	0.2IOVCC	V	-
Output High Voltage	$V_{OH}$	0.8IOVCC	-	IOVCC	V	-
Output Low Voltage	$V_{OL}$	GND	-	0.2IOVCC	V	-
I/O Leak Current	lu	-1.0	-	1.0	uA	-

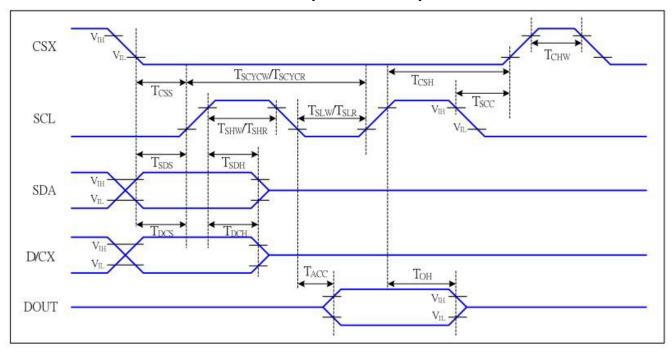
Note: Voltage greater than above may damage the module. All voltages are specified relative to VSS=0V.

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### 7. Timing Characteristics

### 7.1 Serial Interface Characteristics (4-line serial):



Signal	Symbol	Parameter	MIN	MAX	Unit	Description
	T <sub>CSS</sub>	Chip select setup time (write)	15		ns	
	T <sub>CSH</sub>	Chip select hold time (write)	15		ns	
CSX	T <sub>CSS</sub>	Chip select setup time (read)	60		ns	]
	T <sub>SCC</sub>	Chip select hold time (read)	65		ns	]
	T <sub>CHW</sub>	Chip select "H" pulse width	40		ns	
	T <sub>SCYCW</sub>	Serial clock cycle (Write)	66		ns	write command 9 date
	T <sub>SHW</sub>	SCL "H" pulse width (Write)	15		ns	-write command & data
T <sub>SLW</sub>	SCL "L" pulse width (Write)	15		ns	ram	
SCL	T <sub>SCYCR</sub>	Serial clock cycle (Read)	150		ns	read command 0 date
T <sub>SHR</sub>	T <sub>SHR</sub>	SCL "H" pulse width (Read)			ns	-read command & data
	T <sub>SLR</sub>	SCL "L" pulse width (Read)	60		ns	ram
D/CX	T <sub>DCS</sub>	D/CX setup time	10		ns	
DICX	T <sub>DCH</sub>	D/CX hold time	10		ns	
SDA	T <sub>SDS</sub>	Data setup time	10		ns	
(DIN)	T <sub>SDH</sub>	Data hold time	10		ns	
DOUT	T <sub>ACC</sub>	Access time	10	50	ns	For maximum CL=30pF
DOOT	Тон	Output disable time	15	50	ns	For minimum CL=8pF

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### 7.2 i80-System Interface Timing Characteristics

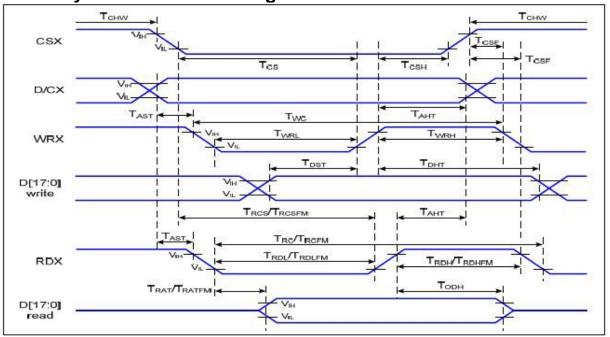


Figure 1 Parallel Interface Timing Characteristics (8080-Series MCU Interface)

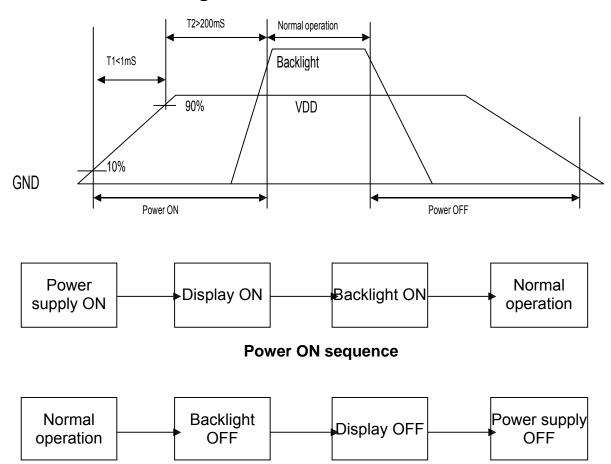
VDDI=1.65 to 3.3V, VDD=2.4 to 3.3V, AGND=DGND=0V, Ta= -30 to 70 ℃

Signal	Symbol	Parameter	Min	Max	Unit	Description	
D/CX T <sub>AST</sub>		Address setup time	0	33	ns	900.00	
		Address hold time (Write/Read)	10		ns	-	
***	T <sub>CHW</sub>	Chip select "H" pulse width	0		ns		
	T <sub>CS</sub>	Chip select setup time (Write)	15		ns		
CCV	T <sub>RCS</sub>	Chip select setup time (Read ID)	45	0.5	ns		
CSX	T <sub>RCSFM</sub>	Chip select setup time (Read FM)	355	37 E	ns		
	T <sub>CSF</sub>	Cose Chip select wait time (Write/Read)			ns		
T <sub>CSH</sub>		Chip select hold time	10		ns		
	T <sub>wc</sub>	Write cycle	66		ns		
WRX T <sub>WRH</sub>		Control pulse "H" duration	15	8 8 8 9	ns		
	T <sub>WRL</sub>	Control pulse "L" duration	15		ns		
	T <sub>RC</sub>	Read cycle (ID)	160		ns		
RDX (ID)	T <sub>RDH</sub>	Control pulse "H" duration (ID)	90	25 25	ns	When read ID data	
T <sub>RDL</sub>		Control pulse "L" duration (ID)	45		ns		
DDV	T <sub>RCFM</sub>	Read cycle (FM)	450		ns	W	
RDX	T <sub>RDHFM</sub> Control pulse "H" duration (FM)		90		ns	When read from	
(FM)	T <sub>RDLFM</sub> Control pulse "L" duration (FM)		355	2	ns	frame memory	
D[17:0]	T <sub>DST</sub>	Data setup time	10	8 8	ns	For CL=30pF	

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### 7.3 Power ON/OFF Timing

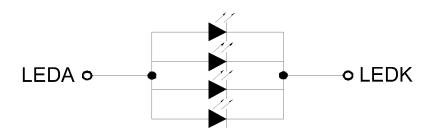


**Power OFF sequence** 

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### 8. Backlight Characteristic



BL CIRCUIT DIAGRAM:

Item	Symbol	MIN	TYP	MAX	UNIT	Test Condition
Supply Voltage	Vf	-	3.2	-	V	lf=80mA
Supply Current	If	-	80	-	mA	-
Luminous Intensity for LCM	-	300	350	-	cd/m <sup>2</sup>	If=80mA
Uniformity for LCM	-	70	80	-	%	lf=80mA
Life Time	-	20000	-	-	Hr	lf=80mA
Backlight Color	White					



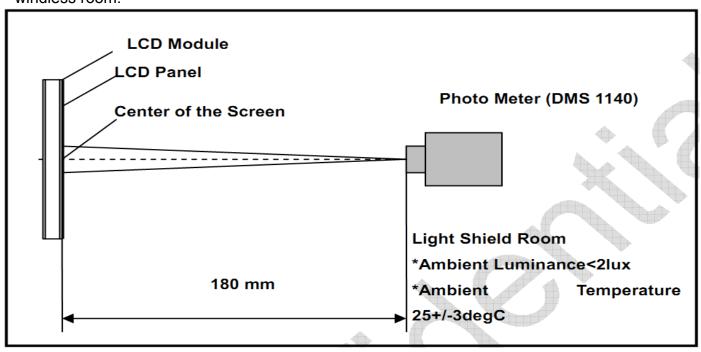
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9. Optical Characteristics

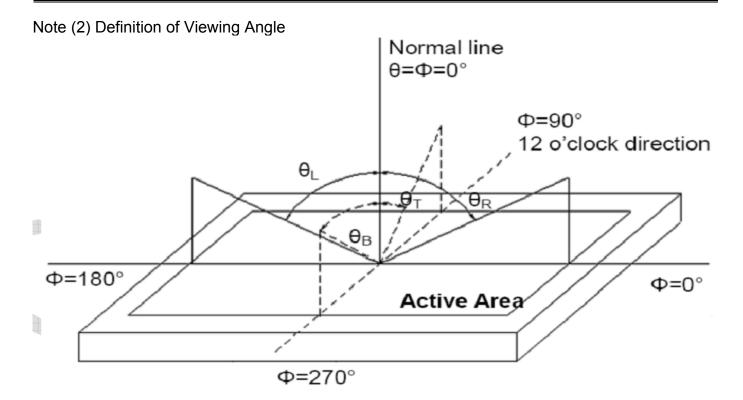
Item	Conditions		Min.	Тур.	Max.	Unit	Note	
	Horizontal	θL	80	-	-			
Viewing Angle	Horizoniai	θR	80	-	-	dograa	(1) (2) (6)	
(CR>10)	Vertical	θт	80	-	-	degree	(1),(2),(6)	
	vertical	θв	80	-	-			
Contrast Ratio	Center		700	900	-	-	(1),(3),(6)	
Response Time	Rising			30	35	mo	(1) (4) (6)	
	Falling		-	30	35	ms	(1),(4),(6)	
	Red x			TBD		-		
	Red y			TBD		-		
	Green x			TBD		-		
CF Color	Green y			TBD		-	(1) (6)	
Chromaticity (CIE1931)	Blue x		Тур.	TBD	Тур.		(1), (6)	
,	Blue y		-0.05	TBD	+0.05	-		
	White x			TBD		-		
	White y			TBD		-		
Transmittance	-		-	5.5	-	%	(1),(5),(6)	

Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.



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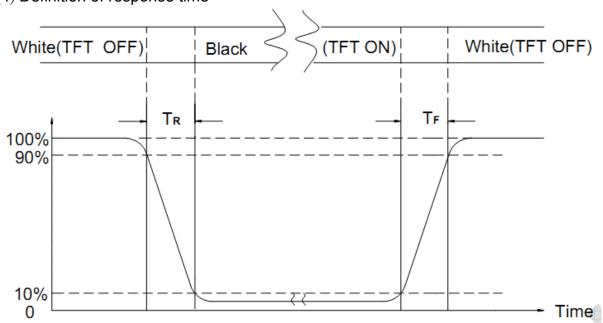


Note (3) Definition of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression Contrast Ratio (CR) = L63 / L0

L63: Luminance of gray level 63, L0: Luminance of gray level 0

Note (4) Definition of response time



Note (5) Definition of Transmittance (Module is without signal input)

Transmittance = Center Luminance of LCD / Center Luminance of Back Light x 100%

Note (6) Definition of color chromaticity (CIE1931)

Color coordinates measured at the center point of LCD



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10. Reliability Test Conditions And Methods

Item	Condition	Time (hrs)	Assessment
High temp. Storage	80°C	120	No abnormalities in functions and appearance
High temp. Operating	70°C	120	
Low temp. Storage	-30°C	120	
Low temp. Operating	-20°C	120	
Humidity	40°C/ 90%RH	120	
Thermal Shock(Non-operation)	$-20$ °C $\neg$ 25°C $\rightarrow$ 70°C (0.5 hour $\neg$ 5 min $\rightarrow$ 0.5 hour)	10cycles	



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### 11. Handling Precautions

### 11.1 Mounting method

The LCD panel of AMSON TFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

### 11.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (CI), Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

#### 11.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to POWER or GROUND, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

#### 12.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

#### 11.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
  - Usage under the maximum operating temperature, 50%Rh or less is required.



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#### 11.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
   [It is recommended to store them as they have been contained in the inner container at the time of delivery from us

### 11.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

#### 12. Precaution for Use

#### 12.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

#### 12.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification.
- When a new problem is arisen which is not specified in this specifications?
- When an inspection specifications change or operating condition change in customer is reported to AMSON TFT and some problem is arisen in this specification due to the change.
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

## 13. Packing Method TBD