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# Specification for Approval

| Customer:   |  |
|-------------|--|
| Model Name: |  |

| Sı           | Customer approval |             |  |
|--------------|-------------------|-------------|--|
| R&D Designed | R&D Approved      | QC Approved |  |
| Peter        | Peng Jun          |             |  |



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## **Revision Record**

| A 2017-04-26 NEW ISSUE | REV NO. | REV DATE   | CONTENTS  | Note |
|------------------------|---------|------------|-----------|------|
|                        | Α       | 2017-04-26 | NEW ISSUE |      |
|                        |         |            |           |      |
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#### 1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by AMSON electronics.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution

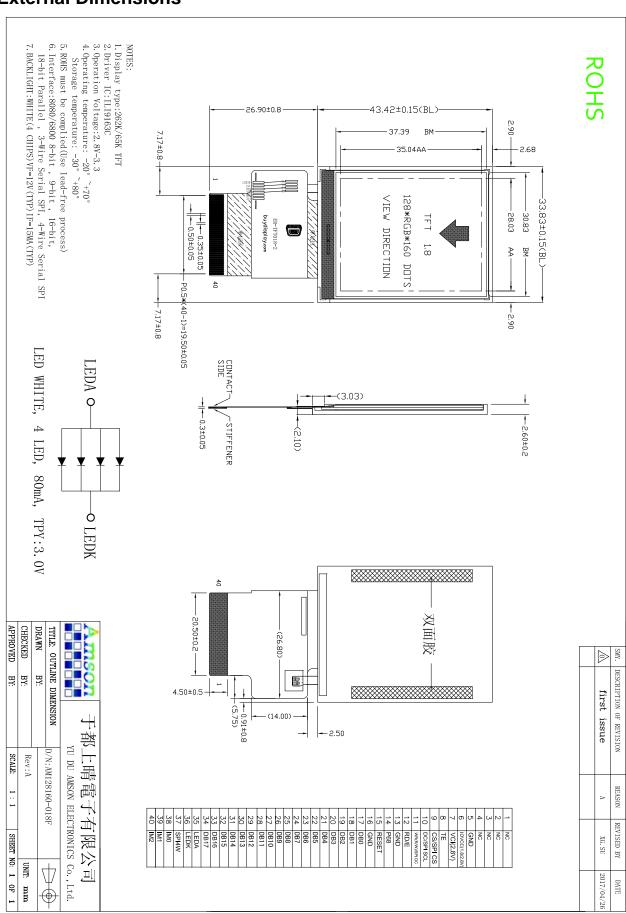
#### 2. General Information

| ITEM                           | STANDARD VALUES                    | UNITS |
|--------------------------------|------------------------------------|-------|
| LCD type                       | 1.77"TFT                           |       |
| Dot arrangement                | 128(RGB)×160                       | dots  |
| Color filter array             | RGB vertical stripe                |       |
| Display mode                   | TN / Transmissive / Normally White |       |
| Gray scale inversion Direction | 12 o'clock                         |       |
| Viewing Direction              | 6 o'clock                          |       |
| Driver IC                      | ILI9163V                           |       |
| Module size                    | 33.83(W)×43.42(H)×2.6(T)           | mm    |
| Active area                    | 28.03(W)×35.04(H)                  | mm    |
| Dot pitch                      | 0.219(W)×0.219(H)                  | mm    |
| Interface                      | MCU / SPI                          |       |
| Operating temperature          | -20 ~ +70                          | °C    |
| Storage temperature            | -30 ~ +80                          | °C    |
| Back Light                     | 4 White LED In Parallel            |       |

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#### 3. External Dimensions





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4. Interface Description

| PIN.NO | SYMBOL                | I/O/P | FUNCTION   |  |  |  |
|--------|-----------------------|-------|--|--|--|--|
| 1      | NC                    | 1     | No connection  |  |  |  |
| 2      | NC                    | -     | No connection  |  |  |  |
| 3      | NC                    | -     | No connection  |  |  |  |
| 4      | NC                    | -     | No connection  |  |  |  |
| 5      | GND                   | Р     | Power Ground   |  |  |  |
| 6      | IOVCC(1.8/2.8V)       | Р     | Power supply for interface logic circuits (1.65 ~ 3.3 V)   |  |  |  |
| 7      | VCI(2.8V)             | Р     | Power supply for analog circuit.  Could connect to external power supply (VCI=2.5~4.0V).   |  |  |  |
| 8      | TE                    | 0     | Tearing effect output pin to synchronies MCU to frame writing, activated by S/W command. When this pin is not activated, this pin is low.  If not used, please open this pin.  |  |  |  |
| 9      | CS / SPI CS           | I     | Chip select input pin ("Low" enable). This pin can be permanently fixed "Low" in MCU interface mode only.  |  |  |  |
| 10     | D/CX / SPI SCL        | I     | Display data / Command selection pin in parallel and SCL in 3pin SPI interface. D/CX='1': Display data. D/CX='0': Command data. If not used, please connect this pin to GND.   |  |  |  |
| 11     | WR(R/W) / SPI<br>D/CX | I     | Write enable in parallel interface. WRX: for 8080 MCU R/WX: for 6800 MCU D/CX: for 4wire SPI If not used, please connect this pin to IOVCC or GND.   |  |  |  |
| 12     | RD/E                  | I     | Read enable in 8080parallel interface and Read/ Write operation enable pin in 6800parallel interface.  In 8080parallel interface, if not used, please connect this pin to IOVCC.  In 6800parallel interface, if not used, please connect this pin to IOVCC or GND. |  |  |  |
| 13     | GND                   | Р     | Power Ground   |  |  |  |
| 14     | P68                   | I     | 8080/6800 MCU Interface mode selection. P68='1': select 6800MCU parallel interface P68='0': select 8080MCU parallel interface If not used, please fix this pin at GND level.   |  |  |  |
| 15     | RESET                 | I     | Chip reset pin ("Low Active"). This signal low will reset the device and must be applied to properly initialize the  |  |  |  |



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|       |          |     | chip.  |  |  |  |  |  |  |
|-------|----------|-----|--|--|--|--|--|--|--|
| 16    | GND      | Р   | Power Ground   |  |  |  |  |  |  |
| 17-34 | DB0-DB17 | I/O | When RCM1='0' (MCU I/F), DB[17:0] are used to MCU parallel interface data bus, and DB0 is also the serial input/ output signal in SPI interface mode. In serial interface, DB[17:1] are not used and should be connected to ground.  When RCM1='1' (RGB I/F), DB[17:0] are used to RGB interface data bus. |  |  |  |  |  |  |
| 35    | LEDA     | Р   | Power for backlight (anode)  |  |  |  |  |  |  |
| 36    | LEDK     | Р   | Power for backlight (cathode)  |  |  |  |  |  |  |
| 37    | SPI4W    | I   | SPI interface selection pin SPI4W='0': 3wire SPI. (default) SPI4W='1': 4wire SPI. This pin is internal pull low.   |  |  |  |  |  |  |
|       |          |     | MCU parallel interface type selection  |  |  |  |  |  |  |
| 38    | IMO      |     | IM1 IM0 Parallel interface   |  |  |  |  |  |  |
|       |          | ı   | 0 0 MCU 8bit Parallel  |  |  |  |  |  |  |
|       |          | '   | 0 1 MCU 16bit Parallel   |  |  |  |  |  |  |
| 39    | IM1      |     | 1 0 MCU 9bit Parallel  |  |  |  |  |  |  |
|       |          |     | 1 1 MCU 18bit Parallel   |  |  |  |  |  |  |
| 40    | IM2      | I   | MCU Parallel interface bus and Serial interface select - IM2='1';Parallel Interface - IM2='0';Serial Interface   |  |  |  |  |  |  |

5. Absolute Maximum Ratings

| Item                  | Symbol | Min. | Max. | Unit |
|-----------------------|--------|------|------|------|
| Analog Supply Voltage | VCI    | -0.3 | 4.0  | V    |
| Logic Supply Voltage  | IOVCC  | -0.3 | 3.3  |      |
| Operating Temperature | Тор    | -20  | 70   | °C   |
| Storage Temperature   | Тѕт    | -30  | 80   | °C   |
| Storage Humidity      | HD     | 20   | 90   | %RH  |

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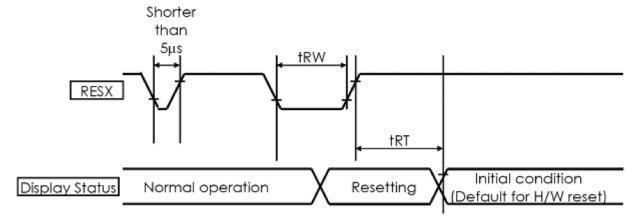
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### 6. DC Characteristics

| Item                  | Symbol          | Min.      | Тур. | Max.      | Unit | Remark              |
|-----------------------|-----------------|-----------|------|-----------|------|---------------------|
| Analog Supply Voltage | VCI             | 2.5       |      | 3.3       | V    |                     |
| Logic Supply Voltage  | IOVCC           | 1.65      |      | 3.3       | V    |                     |
| Input High Voltage    | V <sub>IH</sub> | 0.7IOVCC  |      | IOVCC     | V    | Digital input pins  |
| Input Low Voltage     | $V_{IL}$        | GND       |      | 0.3 IOVCC | V    | Digital input pins  |
| Output High Voltage   | $V_{OH}$        | 0.8 IOVCC |      | IOVCC     | V    | Digital output pins |
| Output Low Voltage    | $V_{OL}$        | GND       |      | 0.2 IOVCC | V    | Digital output pins |
| I/O Leak Current      | lu              | -0.1      |      | 0.1       | uA   |                     |

### 7. Timing Characteristics

### 7.1 Reset Timing Characteristics

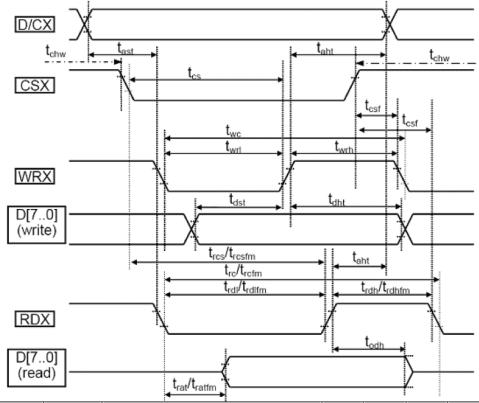


| Symbol | Parameter                       | Related | MIN | TYP   | MAX | Note                  | Unit |
|--------|---------------------------------|---------|-----|-------|-----|-----------------------|------|
|        |                                 | Pins    |     |       |     |                       |      |
| tRESW  | *1) Reset low pulse width       | RESX    | 10  | -     | -   | -                     | μs   |
|        |                                 |         |     |       | 5   | When reset applied    | ms   |
| tREST  | TOT (10) Decent consulate width |         | _   | -   - | 3   | during Sleep in mode  |      |
| IKEST  | *2) Reset complete width        |         |     |       | 120 | When reset applied    | ms   |
|        |                                 | -       | -   | _     | 120 | during Sleep out mode |      |

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#### 7.2 Parallel MCU 18/16/9/8-bit Bus

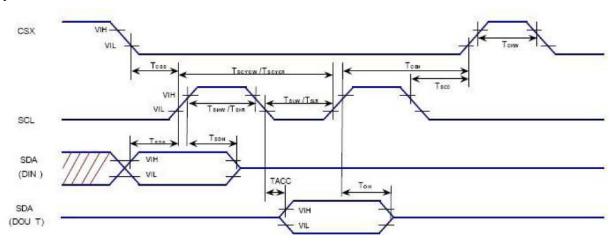


| Signal    | Symbol | Parameter                         | min | max | unit | description           |
|-----------|--------|-----------------------------------|-----|-----|------|-----------------------|
| D/CV      | tast   | Address setup time                | 0   |     | ns   |                       |
| D/CX taht |        | Address hold time(Write/Read)     | 10  |     | ns   |                       |
|           | tchw   | "S""H" Pulse Widtch               | 0   |     | ns   |                       |
|           | tcs    | Chip Select setup time (Write)    | 10  |     | ns   |                       |
| CSX       | trcs   | Chip Select setup time (Read ID)  | 45  |     | ns   |                       |
|           | trosfm | Chip Select setup time (Read FM)  | 355 |     | ns   |                       |
|           | tcsf   | Chip Select Wait time(Write/read) | 10  |     | ns   |                       |
|           | twc    | Write cycle                       | 66  |     | ns   |                       |
| WRX       | twrh   | Controlpulse H duration           | 15  |     | ns   |                       |
|           | twrl   | Control pulse L duration          | 15  |     | ns   |                       |
|           | trc    | Read cycle (ID)                   | 160 |     | ns   | When read ID          |
| RDX       | trdh   | Control pulse H duration(ID)      | 90  |     | ns   | data                  |
|           | trdl   | Control pulse L duration(ID)      | 45  |     | ns   | uata                  |
|           | trcfm  | Read cycle (FM)                   | 450 |     | ns   | When read from        |
| RDX       | trdhfm | Control pulse H duration (FM)     | 90  |     | ns   | frame memory          |
|           | trdlfm | Control pulse L duration (FM)     | 355 |     | ns   | manie memory          |
|           | tdst   | Data setup time                   | 10  |     | ns   | For movimum           |
|           | tdht   | Data hold time                    | 10  |     | ns   | For maximum CL = 30pF |
| D[170]    | trat   | Read access time (ID)             |     | 40  | ns   | For minimum           |
|           | tratfm | Read access time (FM)             |     | 340 | ns   | CL = 8pF              |
|           | todh   | Output disable time               | 20  | 80  | ns   | ос – орг              |

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#### 7.3 3-pin Serial Interface

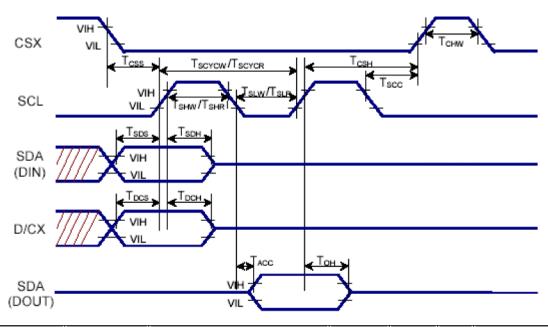


| Signal   | Symbol | Parameter                   | MIN | MAX | Unit | Description           |
|----------|--------|-----------------------------|-----|-----|------|-----------------------|
|          | TCSS   | Chip select setup time      | 10  |     | ns   |                       |
| CSX      | TCSH   | Chip select hold time       | 30  |     | ns   |                       |
|          | TCHW   | Chip select "H" pulse width | 30  |     | ns   |                       |
|          | TSCYCW | Serial clock cycle(Write)   | 66  |     | ns   |                       |
|          | TSHW   | S"L""H" pulse width(Write)  | 15  |     | ns   |                       |
| SCL      | TSLW   | S"L""L" pulse width(Write)  | 15  |     | ns   |                       |
| SCL      | TSCYCR | Serial clock cycle(Read)    | 150 |     | ns   |                       |
|          | TSHR   | S"L""H" pulse width(Read)   | 60  |     | ns   |                       |
|          | TSLR   | S"L""L" pulse width(Read)   | 60  |     | ns   |                       |
|          | TSDS   | Data setup time             | 5   |     | ns   |                       |
| SDA(DIN) | TSDH   | Data hold time              | 5   |     | ns   |                       |
| (DOUT)   | TACC   | Access time                 | 5   | 50  | ns   | For maximum CL = 30pF |
|          | ТОН    | Output disable time         | 10  |     | ns   | For minimum CL = 8pF  |

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#### 7.4 4-pin Serial Interface

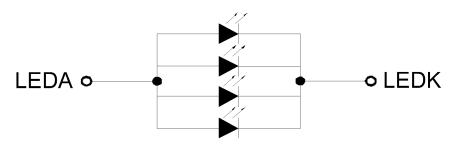


| Signal   | Symbol | Parameter                   | MIN | MAX | Unit | Description           |
|----------|--------|-----------------------------|-----|-----|------|-----------------------|
|          | TCSS   | Chip select setup time      | 10  |     | ns   |                       |
| CSX      | TCSH   | Chip select hold time       | 30  |     | ns   |                       |
|          | TCHW   | Chip select "H" pulse width | 30  |     | ns   |                       |
|          | TSCYCW | Serial clock cycle(Write)   | 66  |     | ns   |                       |
|          | TSHW   | S"L""H" pulse width(Write)  | 15  |     | ns   |                       |
| SCL      | TSLW   | S"L""L" pulse width(Write)  | 15  |     | ns   |                       |
| SCL      | TSCYCR | Serial clock cycle(Read)    | 150 |     | ns   |                       |
|          | TSHR   | S"L""H" pulse width(Read)   | 60  |     | ns   |                       |
|          | TSLR   | S"L""L" pulse width(Read)   | 60  |     | ns   |                       |
| D/CX     | TDCS   | D/CX setup time             | 5   |     | ns   |                       |
| D/CX     | TDCH   | D/CX hold time              | 5   |     | ns   |                       |
|          | TSDS   | Data setup time             | 5   |     | ns   |                       |
| SDA(DIN) | TSDH   | Data hold time              | 5   |     | ns   |                       |
| (DOUT)   | TACC   | Access time                 | 5   | 50  | ns   | For maximum CL = 30pF |
|          | ТОН    | Output disable time         | 10  |     | ns   | For minimum CL = 8pF  |

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### 8. Backlight Characteristics



### BL CIRCUIT DIAGRAM:

| Item                       | Symbol | MIN   | TYP | MAX | UNIT              | Test Condition |
|----------------------------|--------|-------|-----|-----|-------------------|----------------|
| Supply Voltage             | Vf     | 2.7   | 3.0 | 3.3 | V                 | If=80mA        |
| Supply Current             | lf     |       | 80  |     | mA                |                |
| Luminous Intensity for LCM |        |       | 250 |     | Cd/m <sup>2</sup> | If=80mA        |
| Uniformity for LCM         |        | 80%   |     |     | %                 | If=80mA        |
| Life Time                  |        | 20000 |     |     | Hr                | If=80mA        |
| Backlight Color            | White  |       |     |     |                   |                |



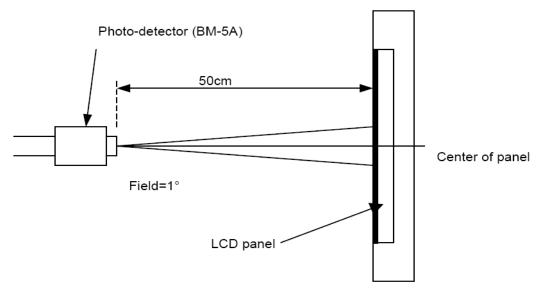
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9. Optical Characteristics

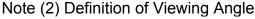
| Item                      | Condition           | s  | Min.  | Тур. | Max.   | Unit   | Note        |  |
|---------------------------|---------------------|----|-------|------|--------|--------|-------------|--|
|                           | Horizontal          | θL | 40    | 45   | -      |        |             |  |
| Viewing Angle             | Horizoniai          | θR | 40    | 45   | -      |        | (4) (2) (6) |  |
| (CR>10)                   | Vertical            | θт | 40    | 45   | -      | degree | (1),(2),(6) |  |
|                           | vertical            | θв | 15    | 20   | -      |        |             |  |
| Contrast Ratio            | Center              |    | 200   | 300  | -      | -      | (1),(3),(6) |  |
| Response Time             | Rising + Falling    |    | -     | 30   | 60     | ms     | (1),(4),(6) |  |
|                           | Red x Red y Green x |    |       | TBD  |        | -      |             |  |
|                           |                     |    |       | TBD  |        | -      |             |  |
|                           |                     |    |       | TBD  |        | -      |             |  |
| CF Color                  | Green y             |    | Тур.  | TBD  | Тур.   | -      | (1) (6)     |  |
| Chromaticity<br>(CIE1931) | Blue x              |    | -0.05 | TBD  | +0.05  | -      | (1), (6)    |  |
|                           | Blue y              |    |       | TBD  |        | -      |             |  |
|                           | White x<br>White y  |    |       | TBD  | -<br>- | -      |             |  |
|                           |                     |    |       | TBD  |        | -      |             |  |

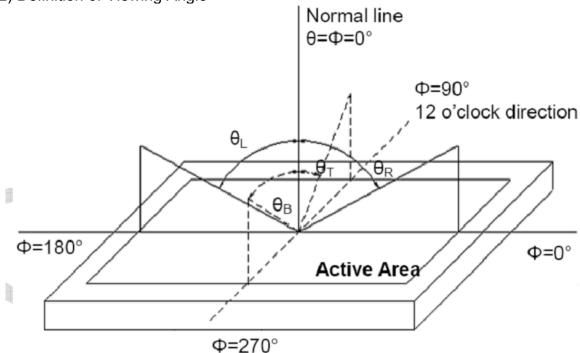
Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.



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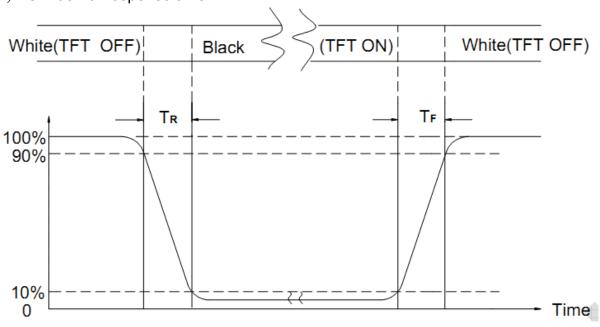


Note (3) Definition Of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression Contrast Ratio (CR) = L63 / L0

L63: Luminance of gray level 63, L0: Luminance of gray level 0

#### Note (4) Definition of response time



Note (5) Definition of Transmittance (Module is without signal input)

Transmittance = Center Luminance of LCD / Center Luminance of Back Light x 100%

Note (6) Definition of color chromaticity (CIE1931)

Color coordinates measured at the center point of LCD



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#### 10. Reliability Test Conditions and Methods

No change on display and in operation under the following test condition.

Condition: Unless otherwise specified, tests will be conducted under the following condition.

Temperature: 20±5°C Humidity: 65±5%RH Tests will be not conducted under functioning state.

| No. | Parameter   |  |           |  |  |
|-----|---|--|-----------|--|--|
| 1   | High Temperature Operating                                  | 70°C±2°C, 240hrs (Operation state)   | Notes<br> |  |  |
| 2   | Low Temperature Operating                                   | -20°C±2°C, 240hrs (Operation state)  |           |  |  |
| 3   | High Temperature<br>Storage                                 | 80°C±2°C, 240hrs   |           |  |  |
| 4   | Low Temperature<br>Storage                                  | -30°C±2°C, 240hrs  |           |  |  |
| 5   | High Temperature and<br>High<br>Humidity Operation Tes<br>t | 60°C±2°C, 90%, 240hrs  |           |  |  |
| 6   | Vibration Test  | Total fixed amplitude: 1.5mm Vibration Frequency: 10~55Hz One cycle 60 seconds to 3 direction of X, Y, Z each 15 minutes.  |           |  |  |
| 7.  | Drop Test   | To be measured after dropping from 60cm high on the concrete surface in packing state.  F Dropping method corner dropping A corner: once Edge dropping B, C, D edge: once Face dropping E, F, G face: once  Concrete Surface |           |  |  |

Notes:

- 1. No dew condensation to be observed.
- 2. The function test shall be conducted after 4 hours storage at the normal temperature and humidity after removed from the test chamber.
- 3. Vibration test will be conducted to the product itself without putting I in a container.



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#### 11. Inspection Standard

#### 11.1.1 Inspection conditions

Inspection performed under the following conditions is recommended.

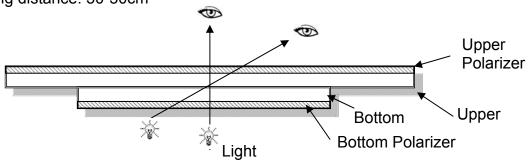
Temperature: 25±5°C

Humidity: 65%±10%RH

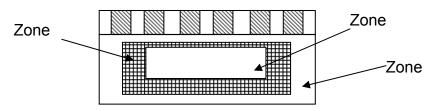
Viewing Angle: Normal viewing Angle.

Illumination: Single fluorescent lamp (300 to 700Lux)

Viewing distance: 30-50cm



#### 11.1.2 Definition



Zone A: Effective Viewing Area (Character or Digit can be seen)

Zone B: Viewing Area except Zone A

Zone C: Outside (Zone A + Zone B) which cannot be seen after assembly by customer.)

Note:

As a general rule, visual defects in Zone C can be ignored when it doesn't effect product function or appearance after assembly by customer.

#### 11.1.3 Sampling Plan

According to GB/T 2828-2003; normal inspection, Class  $\scriptstyle \rm II$  AQL:

| Major defect | Minor defect |
|--------------|--------------|
| 0.65         | 1.5          |

LCD: Liquid Crystal Display, TP: Touch Panel, LCM: Liquid Crystal Module

| No | Items to be inspected | Criteria  | Classification of defects |
|----|-----------------------|---|---------------------------|
| 1  | Functional defects    | <ol> <li>No display, Open or miss line</li> <li>Display abnormally, Short</li> <li>Backlight no lighting, abnormal lighting.</li> <li>TP no function</li> </ol> | Major                     |
| 2  | Missing               | Missing component   |                           |



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| 3 | Outline dimension    | Overall outline dimension beyond the drawing is not allowed |       |
|---|----------------------|---|-------|
| 4 | Color tone           | Color unevenness, refer to limited sample                   |       |
| 5 | Soldering appearance | Good soldering, Peeling off is not allowed.                 | Minor |
| 6 | LCD/Polarizer/TP     | Black/White spot/line, scratch, crack, etc.                 |       |

| 11.1.4 Criteria (Visual)  |                            |  |  |  |  |  |  |  |
|---|----------------------------|--|--|--|--|--|--|--|
| Number  | Items                      | Criteria(mm)   |  |  |  |  |  |  |
|   | (1) The edge of LCD broken | X Y Z  ≤3.0mm  |  |  |  |  |  |  |
|   |                            | line of the seal   |  |  |  |  |  |  |
| 1.0 LCD Crack / Broken  NOTE: X: Length Y: Width Z: Height L: Length of ITO, T: Height of LCD | (2)LCD corner broken       | X         Y         Z           ≤3.0mm         ≤L         ≤T |  |  |  |  |  |  |
|   | (3) LCD crack              | Crack<br>Not allowed   |  |  |  |  |  |  |



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| Number | Items                                   | Criteria (mm)  |                     |               |           |               |  |  |
|--------|---|--|---------------------|---------------|-----------|---------------|--|--|
|        |   | ① light dot (LCD/TP/Polarizer black/white spot , light dot, pinhole,             |                     |               |           |               |  |  |
|        |   | dent, stain)   |                     |               |           |               |  |  |
|        |   | Zone   | Ac                  | ceptable C    | ety       | /             |  |  |
|        |   | Size (mm)  | Α                   | В             | С         |               |  |  |
|        |   | Ф≤0.10   | Ignore              |               |           |               |  |  |
|        |   | 0.10<Φ≤0.15  | 3( distance≧10mm)   |               | lanor     |               |  |  |
|        |   | 0.15<Φ≤0.2   | 1                   |               | - Ignor   | 6             |  |  |
|        |   | 0.2<Ф  | 0                   |               |           |               |  |  |
|        | Spot defect                             | ②Dim spot(LCI  | D/TP/Polarize       | r dim dot, li | ght leaka | ge、dark spot) |  |  |
|        | <u> </u>                                | Zone   | Ac                  | ceptable C    | ty        |               |  |  |
|        | V V V                                   | Size (mm)  | Α                   | В             | С         |               |  |  |
|        |   | Ф≤0.1  | Igno                | re            |           |               |  |  |
|        | X<br>Φ=(X+Y)/2                          | 0.1<Φ≤0.2  | 2( distance ≥ 10mm) |               | lanor     |               |  |  |
|        |   | 0.2<Φ≤0.3  | 1                   |               | - Ignor   | e             |  |  |
| 2.0    |   | Ф>0.3  | 0                   |               |           |               |  |  |
|        |   | ③ Polarizer accidented spot  |                     |               |           |               |  |  |
|        |   | Zone   | A                   | cceptable (   | Qty       | ty            |  |  |
|        |   | Size (mm)  | А                   | В             | С         |               |  |  |
|        |   | Ф≤0.2  | Igno                | ore           |           |               |  |  |
|        |   | 0.2<Φ≤0.5  | 2( distance         | e≧10mm)       | Igno      | re            |  |  |
|        |   | Ф>0.5  | 0                   |               |           |               |  |  |
|        |   |  |                     |               |           |               |  |  |
|        |   | \\/idth/mm\  | Length(m            | Accep         | table Qty |               |  |  |
|        | Line defect                             | Width(mm)  | m)                  | А             | В         |               |  |  |
|        | (LCD/TP<br>/Polarizer                   | Ф≤0.03   | Ignore              | Ignore        | !         |               |  |  |
|        | black/white<br>line, scratch,<br>stain) | 0.03 <w≤0.05< td=""><td>L≤3.0</td><td>N≤2</td><td>lgn</td><td>ore</td></w≤0.05<> | L≤3.0               | N≤2           | lgn       | ore           |  |  |
|        |   | 0.05 <w≤0.08< td=""><td>L≤2.0</td><td>N≤2</td><td></td><td></td></w≤0.08<>       | L≤2.0               | N≤2           |           |               |  |  |
|        |   | 0.08 <w< td=""><td>Defi</td><td>ne as spot</td><td>defect</td><td></td></w<>     | Defi                | ne as spot    | defect    |               |  |  |
|        |   |  |                     |               |           |               |  |  |



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|     |                     | Zone   | ,                |        |        |  |
|-----|---------------------|--|------------------|--------|--------|--|
|     |                     | Size (mm)  | Α                | В      | С      |  |
| 3.0 | Polarizer<br>Bubble | Ф≤0.2  | lgr              | Ignore |        |  |
| 3.0 | Bubble              | 0.2<Φ≤0.4  | 2(distance≥10mm) |        | Ignore |  |
|     |                     | 0.4<Φ≤0.6  | 1                |        |        |  |
|     |                     | 0.6<Ф  | 0                |        |        |  |
|     |                     |  |                  |        |        |  |
| 4.0 | SMT                 | According to IPC-A-610C class II standard. Function defect missing part are major defect, the others are minor defect. |                  |        |        |  |



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#### 12. Handling Precautions

#### 12.1 Mounting method

The LCD panel of AMSON TFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

#### 12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (CI), Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

#### 12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to VDD or GND, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

#### 12.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

#### 12.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
  - Usage under the maximum operating temperature, 50%Rh or less is required.



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#### 12.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
   [It is recommended to store them as they have been contained in the inner container at the time of delivery from us

#### 12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

#### 13. Precaution for Use

#### 13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

#### 13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to AMSON TFT, and some problem is arisen in this specification due to the change
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

### 14. Packing Method

#### **TBD**