

5SC3528V14J0CK01

- **♦**Outline(L*W*H): 3.5*2.8*1.9mm
- **♦**High flux efficiency & Energy conservation
- **◆**Good thermal dissipation & optical uniformity



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Features

- Forward current: ≤70mA
- Typical view angle 50% Iv: 120°
- RoHS2.0 and REACH-compliant
- Lens color: water transparent
- Qualified according to JEDEC moisturevity
 Level 2a
- ESD level 2kV(HBM)
- Reliability Test: AEC Q-102qualified

Applications

- Indoor lighting applications
- Flat backlight for LCD. Switch and symbol
- Indicator and backlighting for all consumer electronics
- Automotive electronics
- Others applications



Product Code Method

5 - S - C -3528 - V14J - 0 - C - K - 01

1 2 3 4 5 6 7 8 9

1	2	3	4	(5)
Process Type	Category	LED Type	Lead Frame Size	Dice wavelength & luminous rank
5 : special product	S: SMD LED	C: PLCC top view D: PLCC side view	3528: 3.5*2.8mm	Vxxx: red

6	7	8	9
Lap Polarity	Cap Color	PCB Module Code	Flow Code
0: non-common anode and non-common cathode	C: water transparent	K: article mode	01: no expression above meaning for company

■ Maximum Rating(Ta=25°C)

Characteristics	Symbol	Typical	Unit
DC Forward Current	I_{F}	70	mA
Pulse Forward Current*3	I_{PF}	150	mA
Reverse Voltage	V_R	10	V
Junction Temperature	T_{J}	125	°C
Operating Temperature Range	Тор	-40-105	°C
Storage Temperature Range	T_{STG}	-40-105	°C
Soldering Temperature*4	T_{SD}	260	°C
Thermal Resistance Junction/ Solder Point	$\mathrm{RTH}_{\mathrm{J-S}}$	100	°C/W
Thermal Resistance Junction/ Ambient Point	$\mathrm{RTH}_{ ext{J-A}}$	200	°C/W

Notes 1: There is no maximum or typical voltage parameter

- 2: For other ambient, limited setting of current will be depended on de-rating curves.
- 3: Duty 1/10, pulse width 0.1ms
- 4: The maximum of soldering time is 10 seconds in T_{SD}



■ Typical Product Characteristics(Ta=25°C)

Characteristics	Symbol	Min.	Тур.	Max.	Unit	Test condition
Forward Voltage	V_{F}	2.0	2.2	2.6	V	I _F =70mA
Luminous Intensity	Iv	2200	3650	-	mcd	I _F =70mA
Dominant Wavelength	λd	610	-	620	nm	I _F =70mA
Peak Wavelength	λр	-	621	-	nm	I _F =70mA
Spectral Width 50%	Δλ	-	16	-	nm	I _F =70mA
Reverse Current	I_R	-	-	10	μΑ	$V_R = 10V$
Viewing Angle	$2\theta_{1/2}$	-	120	-	deg	I _F =70mA

Notes: 1. Measurement errors:

Forward Voltage: ± 0.1 V, Luminous Intensity: $\pm 10\%$ Iv, Color Coordinate: ± 0.005 , Viewing Angle $(2\theta_{1/2}) \pm 5\%$

2. Electrical-Optical Characteristics (Ta=25°C)

■ Range of Bins

1). Forward Voltage $(I_F = 70 \text{mA})$

Bin Code	Min. V _F (V)	Max. V _F (V)
D	2.0	2.1
E	2.1	2.2
F	2.2	2.3
G	2.3	2.4
Н	2.4	2.5
I	2.5	2.6

2). Luminous Intensity ($I_F = 70 \text{mA}$)

Bin Code	Min. Iv(mcd)	Max. Iv(mcd)
18	2200	2800
19	2800	3600
20	3600	4600
21	4600	6000

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■ Range of Bins

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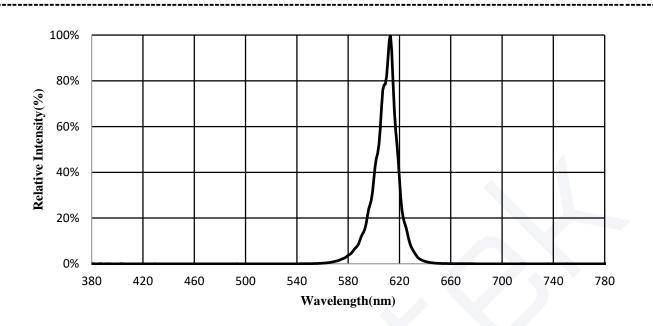
3). Dominant Wavelength Bins (I_F=70mA)

Bin code	Min. λd(nm)	Max. λd(nm)
A	610	615
В	615	620

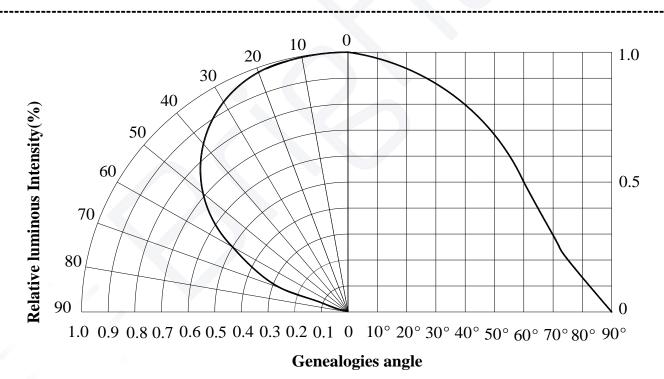
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■ Relative Spectral Power Distribution



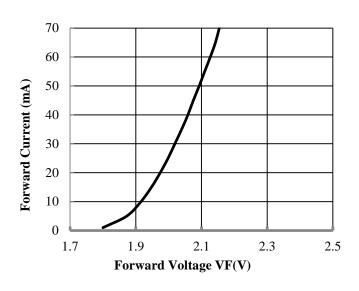
■ Typical Diagram Characteristics of Radiation



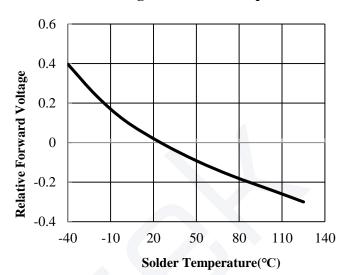


Relative Spectral Power Distribution

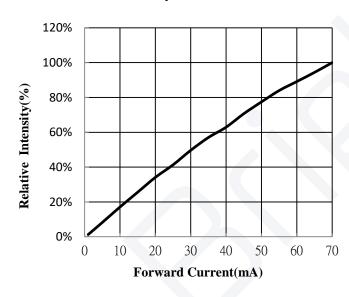
Forward Current vs. Forward Voltage



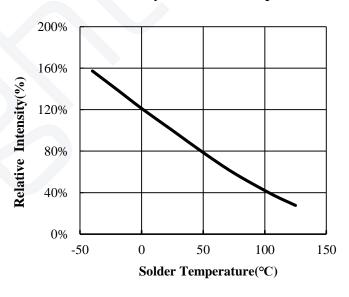
Forward Voltage vs. Solder Temperature



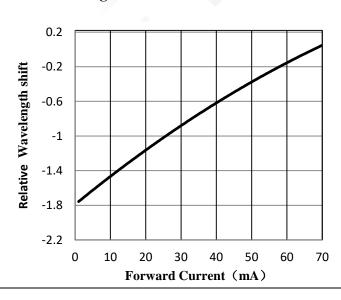
Relative Intensity vs. Forward Current



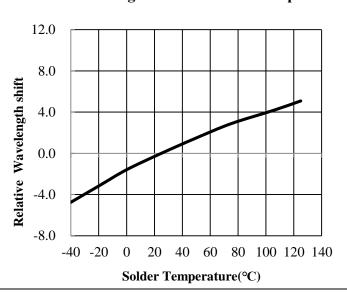
Relative Intensity vs. Solder Temperature



Wavelength shift vs. Forward Current



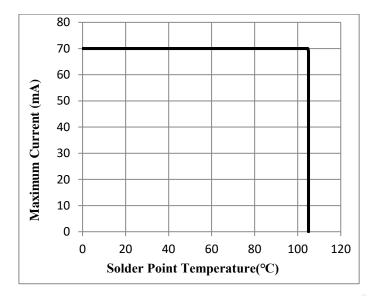
Wavelength shift vs. Solder Temperature





■ Relative Spectral Power Distribution

The maximum forward current is determined by the thermal resistance between the LED junction and solder point. It is crucial for the end product to be designed in a manner that minimizes the thermal resistance from the solder point to ambient in order to optimize lamp life and optical characteristics.

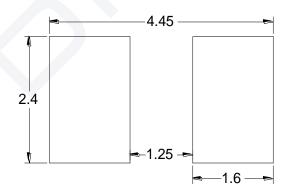




Dimensions

3.2 -**⇔** 0.75 **⋈** → 0.75 R1.2 -1 2 2.8 2.2 1 2 0.15 Top View **Bottom View** Polarity Mark 1.9 **Polarity** Side View

Recommend Padlayout



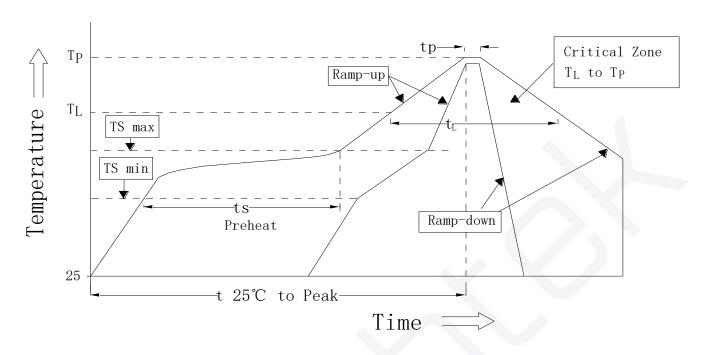
Notes: 1. All dimensions are in millimeters

- 2. Tolerance is ± 0.1 mm unless otherwise noted
- 3. Specifications are subject to change without notice.



■ Reflow Profile

SMT Reflow Soldering Profile



Duofilo Footuus	Carrela al	Pb-F	T T\$4		
Profile Feature	Symbol	Min.	Recommendation	Max.	Unit
Ramp-up rate to preheat (25°C to 150°C)	1-	-	2	3	K/s
Time t_S ($T_{S min}$ to $T_{S max}$)	t_{S}	60	100	120	S
Ramp-up rate to peak $(T_{S \text{ max}} \text{ to } T_P)$	-	-	2	3	K/s
Liquidus temperature	$T_{ m L}$	-	217	-	°C
Time above liquidus temperature	$t_{ m L}$	-	80	100	S
Peak temperature	T _P	-	245	260	°C
Time within 5 °C of the specified peak temperature T _P - 5 K	t _P	-	-	10	S
Ramp-down Rate (T _P to 100 °C)	-	-	3	4	K/s
Time 25 °C to T _P	-	-	-	480	S

Notes:

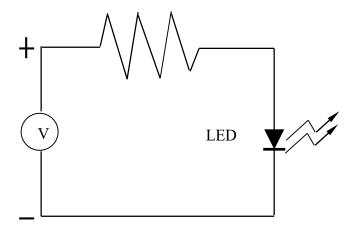
- 1. Do not stress the silicone resin while it is exposed to high temperature.
- 2. The reflow process should not exceed 3 times.



■ Test Circuit and Handling Precautions

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1. Test Circuit



2. Handling Precautions

2.1. Over-current-proof

Customer must apply resistors for protection; otherwise slight voltage shift will cause big current change (Burn out will happen).

2.2. Storage

1). It is recommended to store the products in the following conditions:

Humidity: 60% R.H. Max.

Temperature: $5^{\circ}\text{C} \sim 30^{\circ}\text{C} (41^{\circ}\text{F} \sim 86^{\circ}\text{F})$

2). Shelf life in sealed bag: 12 month at $<5^{\circ}\text{C} \sim 30^{\circ}\text{C}$ and <60% R.H. after the package is Opened, the products should be used within four weeks or they should be keeping to stored at $\leq 20\%$ R.H. with zip-lock sealed.

2.3. Baking

If the package has been opened for more than 4 weeks or over than 12 months in sealed bag. it is recommended to bake the products with the following instruction:

1). $60\pm3^{\circ}$ C X 6hrs and <5%RH, for reel

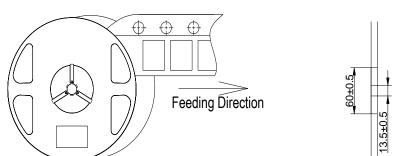
2). $125\pm3^{\circ}$ C X 2hrs, for single LED

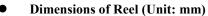
It shall be normal to see slight color fading of carrier (light yellow) after baking in process

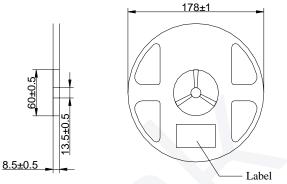


Packing

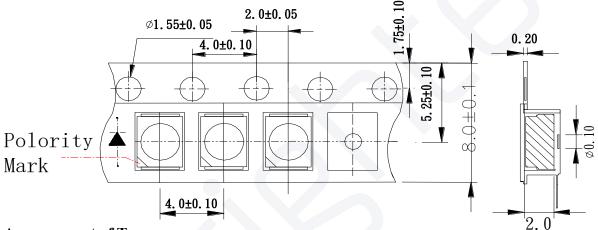
• Feeding Direction (Unit: mm)



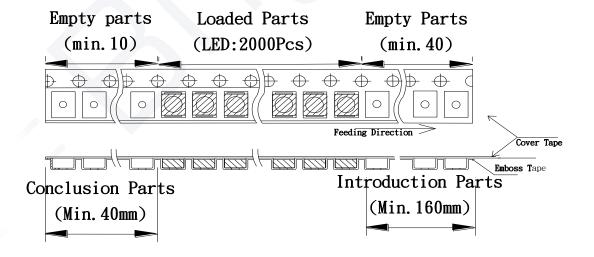




• Dimensions of Tape (Unit: mm)



Arrangement of Tape



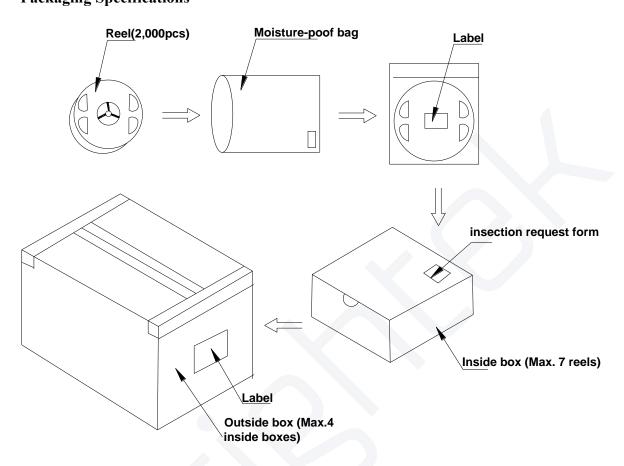
Notes:

- 1. Empty component pockets are sealed with top cover tape
- 2. The max loss number of SMD is 2pcs;
- 3. The cathode is oriented towards the tape sprocket hole in accordance with ANSI/EIA RS-481 specifications;
- 4. 2,000pcs per reel;
- 5. The remainder packing in multiples of 500pcs.



Packing

Packaging Specifications



Notes:

Reeled product (max.2,000) is packed in a sealed moisture-proof bag. Seven bags are packed in an inner box (size: about 260 X 230 X 100 mm) and four inner boxes are in an outer box (size: about 480 X 275 X 215 mm). On the label of moisture-poof bag, there should be the information of Part No., Lot No. and quantity number; also the total quantity number should be on inspection request form on outer box.

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Precautions

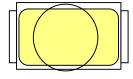
1. Abnormal situation caused by improper setting of collet

To choose the right collet is the key issue in improving the product's quality. LED is different from other electronic components, which is not only about electrical output but also for optical output. This characteristic made LED more fragile in the process of SMT. If the collet's lowering down height is not well set, it will bring damage to the gold wire at the time of collet's picking up and loading which will cause the LED fail to light up, light up now and then or other quality problems

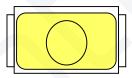
2. How to choose the collet

During SMT, please choose the collet that has larger outer diameter than the lighting area of lens, in case that improper position of collet will damage the gold wire inside the LED. Different collets fit for different products, please refer to the following pictures cross out

Outer diameter of collet should be larger than the lighting area



Picture $1(\sqrt{})$



Picture 2(X)

3. Other points for attention

- A. No pressure should be exerted to the epoxy shell of the SMD under high temperature.
- B. Do not scratch or wipe the lens since the lens and gold wire inside are rather fragile and cross out easy to break.
- C. LED should be used as soon as possible when being taken out of the original package, and should be stored in anti-moisture and anti-ESD package.

4. This usage and handling instruction is only for your reference.

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■ Test Items and Results of Reliability

Test Item	Test Conditions	Duration/ Cycle	Number of Damage	Reference
Thermal Shock	-40°C 30min ↑↓5min 105°C 30min	1000 cycles	0/26	JESD22 A-106
High Temperature Storage	T _a =105°C	1000 hrs	0/26	JESD22 A-103B
Low Temperature Storage	T _a =-40°C	1000 hrs	0/26	JESD22 A-119
Life Test	T _a =25°C If=70mA	1000 hrs	0/26	JESD22 A-108
High Humidity Heat Operation	85°C RH=85% If=70mA	1000 hrs	0/26	JESD22 A-101
High Temperature Operation	T _a =105°C If=70mA	1000 hrs	0/26	JESD22 A-108C
ESD(HBM)	2KV at 1.5kΩ;100pF	3 times	0/30	ANSI/JEDEC JS-001

Failure Criteria					
Item	Symbol	Condition	Criteria for Judgment		
Item	Symbol	Condition	Min	Max	
Forward Voltage	V_{F}	If=70mA	-	USL ¹ ×1.1	
Reverse Current	${ m I}_{ m R}$	$V_R = 10V$	-	10μΑ	
Radiant Power	P_{O}	If=70mA	LSL ² ×0.7	-	

[Note] USL*1: Upper Specification Level

LSL*2: Lower Specification Level

Note: Version updates will not be announced and Brightek will have the final interpretation rights